

Types for Relaxed Memory Models

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Abstract

Multicore computers implementing weak memory models are mainstream, yet type-based analyses of these models remain rare. We help fill this gap. We not only prove the soundness of a type system for a weak execution model, but we also show that interesting properties of that model can be embedded in the types themselves.

We argue that correspondence assertions can be used in a programming discipline that captures happens-before relationships, which are the basis for reasoning about weak memory in Java. This programming discipline is flexible and can be statically enforced. We present several examples from `java.util.concurrent` and prove the static semantics sound with respect to an execution model based on Java’s memory model.

Categories and Subject Descriptors D.3.1 [Programming Languages]: Formal Definitions and Theory

General Terms Theory, Languages

1. Introduction

The C++ memory model posits that programs with data races have undefined semantics [Boehm and Adve 2008]. This model is incompatible with languages such as Java, which allow data races but seek to ensure safety. The Java Memory Model (JMM) [Manson, Pugh, and Adve 2005] is a *speculative model* intended to validate the executions generated by modern processors and compilers while banning *thin-air* reads, and thus to ensuring type safety. To our knowledge, the safety argument has only been made informally.

The *happens-before* relation is crucial to understanding shared-memory concurrent Java programs. The Java APIs contain extensive comments describing the happens-before relation, such as the following: “Actions in a thread prior to placing an object into any concurrent collection happen-before actions subsequent to the access or removal of that element from the collection in another thread.” There is no method for describing happens-before relations formally.

Consider the following tiny example, which shows that, without proper synchronization, it is possible for a thread to read stale values. We adopt the following notational conventions: s and t are thread identifiers; p is an object identifier; the process $s[M]$ denotes a thread with identifier s , executing statement M ; the process $A|B$ denotes the parallel composition of threads A and B .

```
class C1 { int f; int g; }  
s[p.f=42; p.g=27;] |  
t[val y=p.g; val x=p.f; if((y!=0)&&(x==0)){†}]
```

There are two threads sharing object p inhabiting class $C1$ (notation $p:C1$). All fields are initially set to 0. Object p has two fields. Thread s updates $p.f$ then $p.g$. Thread t reads the fields in the reverse order. Because there is no synchronization, the program has data races on both fields.

In a *sequentially consistent* execution [Lamport 1979], threads must appear to execute sequentially in an interleaved fashion. Since s must write $p.f$ before $p.g$, it is impossible for t to read an object from $p.g$ and subsequently read 0 from $p.f$; thus the statement $†$ is unreachable in a sequentially consistent semantics.

However, compilers may re-order unrelated writes and reads, and thus the JMM semantics is designed to allow executions in which $†$ is reachable in example $C1$.

Synchronization eliminates data races, shrinking the set of visible values. For example, in the following variant of example $C1$, field g is declared as `volatile`. There are happens-before edges from a write of a volatile to every subsequent read. Assume $p:C2$.

```
class C2 { int f; volatile int g; }  
s[p.f=42; p.g=27;] |  
t[val y=p.g; val x=p.f; if((y!=0)&&(x==0)){†}]
```

Because of the synchronization though $p.g$, $†$ becomes unreachable in the JMM for $C2$. Such happens-before reasoning is subtle: in the variant where f is volatile but g is not, $†$ is reachable. The reasoning may also fail in the presence of other threads.

In this paper we propose a *compositional program discipline for happens-before* using *correspondence assertions* [Woo and Lam 1992; Gordon and Jeffrey 2003]. As a byproduct, we present a proof of type safety for concurrent Java.

Organization. In Section 2, we sketch the semantics of our memory model and present correspondence assertions as a tool for reasoning about happens-before relations. Section 3 is a brief detour to complete the description of the semantics; we introduce speculation and describe its implications for type soundness. In Section 4, we provide intuitions about the type system via examples. For readability, Sections 1–4 sometimes use Java syntax. For simplicity of formal development, the remainder of the paper uses a more restrictive language.

In Section 5, we define the static type system, which is used to validate class definitions. In Section 6, we define soundness using an operational semantics. In Section 7, we describe the soundness proof, highlighting the most interesting cases of the inductive invariant maintained by the operational semantics. In Section 8, we summarize related work and conclude.

Details of the soundness proof, including the typing rules for processes used in subject reduction, are available in the full version of the paper.

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TLDI’12, January 28, 2012, Philadelphia, PA, USA.

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Acknowledgements. We gratefully acknowledge the detailed comments of the anonymous referees on this and a previous version of the paper. This research was supported by NSF grant #0916741.

2. Happens-before semantics using correspondence assertions

We provide a rough introduction to our operational semantics and describe how correspondence assertions can be integrated to reason about happens-before relations. The semantics is essentially copied from our previous paper [Jagadeesan, Pitcher, and Riely 2010], although it has been adapted to handle atomics.

The semantics is based on the idea that writes do not affect a traditional store, but rather produce *actions*. When a read occurs, the resulting value can be determined by the visible writes. Reduction relates *processes*, which are snapshots of a running system. As a first approximation, one can view a process as a collection of object denotations and threads, surrounded by a sequence of actions. Consider the following reductions using `class Int {int f;}`, where $p: \text{Int}$.

$$\begin{aligned} & p: \text{Int} \mid s[p.f=42; p.f=27;] \mid t[\text{val } x=p.f;] \\ \rightarrow & \langle s!p.f=42 \rangle (p: \text{Int} \mid s[p.f=27;] \mid t[\text{val } x=p.f;]) \\ \rightarrow & \langle s!p.f=42 \rangle \langle s!p.f=27 \rangle (p: \text{Int} \mid s[] \mid t[\text{val } x=p.f;]) \end{aligned}$$

After the first reduction, t can read 42. After the second reduction, t can read either 42 or 27. The read operation is nondeterministic in the latter case.

Java does not have thread or object literals, and so writing Java programs to generate such processes is a bit of a chore. In this case, we must write initialization code that creates the shared object and the threads:

```
class S { Int p; S(Int p) {this.p=p;}
  void run() {p.f=42; p.f=27;}
}
class T { Int p; T(Int p) {this.p=p;}
  void run() {val x=p.f} // using untyped declarations
}
class Main {
  static void main() {
    val p = new Int();
    new S(p).start();
    new T(p).start();
  }
}
```

For expository purposes, we consistently elide this bootstrap code, looking instead at a literal expression of the resulting process.

Once running, a process context defines a sequence of actions (those leading up to the context hole) and a happens-before relation over that sequence. For example, if

$$\mathbb{C} = \langle s!p.f=42 \rangle \langle s!p.f=27 \rangle (p: \text{Int} \mid \llbracket - \rrbracket)$$

then there are two write actions in the sequence, with $\langle s!p.f=42 \rangle$ happening-before $\langle s!p.f=27 \rangle$. The same context can provide different values to different threads. For example, \mathbb{C} justifies reading either 42 or 27 from $p.f$ for thread t ; whereas \mathbb{C} can only justify reading 27 from $p.f$ for thread s . We formalize this by adding an action for the context hole, labeled by the identifier of the thread within the hole that is attempting to read.

The sequence of actions from a single thread defines *program order*. The sequence of actions on a single synchronization variable defines *synchronization order*. Happens-before is defined to be the transitive closure of program order and synchronization order. A write action is available to a read unless it happens-before another write action that in turn happens-before the read; the “other” write *intervenes* between the first write and the read, hiding the first write.

We discuss synchronization order using Java’s *atomic* objects, rather than *volatiles*. A volatile field can be viewed as a final atomic upon which we only call `get` and `set`. In addition, atomics have `get-and-set` and `compare-and-swap` methods, which can be used to implement locks.

Synchronization order specifies that a “set” on an atomic happens-before any subsequent “get.” We model this using synchronization actions of the form $\langle \ell!s:j \rangle$, where ℓ the atomic object acted upon, s is the acting thread, and j is an *opcount*. If the opcount is odd, then the operation was a set. If the opcount is even, then the operation was a get. The opcount is incremented when a set follows a get or a get follows a set. Initially, atomics are “set” with opcount 1.

Reading an atomic is deterministic; thus we store the value held by an atomic with its denotation, rather than representing it using actions. The form of an atomic denotation is `Atomic{v;j}`, where v is the current value and j is the opcount.

Consider the following variant of example C2 from the introduction.

$$\begin{aligned} & p: \text{Int} \mid l: \text{Atomic}\{0;1\} \mid \\ & s[p.f=42; l.set(27);] \mid \\ & t[\text{val } y=l.get(); \text{val } x=p.f; \text{if}((x==0)\&\&(y!=0))\{\dagger\}] \end{aligned}$$

Just as for example C1, statement \dagger is unreachable. For example, consider the case where thread s executes before t . In this case, the read of $p.f$ occurs in the following context.

$$\begin{aligned} & \langle s!p.f=42 \rangle \langle s!l:1 \rangle \langle t!l:2 \rangle \\ & (p: \text{Int} \mid l: \text{Atomic}\{27;2\} \mid s[] \mid \llbracket - \rrbracket) \end{aligned}$$

The synchronization action $\langle s!l:1 \rangle$ was created when s performed `l.set(27)`; this operation modified the value of the atomic, but not its opcount, resulting in `Atomic{27;1}`. The next synchronization action, $\langle t!l:2 \rangle$, was created when t performed `l.get()`; this operation modified the opcount of the atomic, but not its value, resulting in `Atomic{27;2}`. Note that synchronization actions record the opcount, whereas write actions, such as $\langle s!p.f=42 \rangle$, record the value written.

The above context has three happens-before edges, when interpreted as a context for t .

- program order from $\langle s!p.f=42 \rangle$ to $\langle s!l:1 \rangle$,
- synchronization order from $\langle s!l:1 \rangle$ to $\langle t!l:2 \rangle$, and
- program order from $\langle t!l:2 \rangle$ to the hole.

Happens-before is also affected by thread creation. For example, the bootstrap code given above will evaluate to the following context, holding process $s[\dots] \mid t[\dots]$.

$$\begin{aligned} & (\nu m)(\nu p)(\nu s)(\nu t) \\ & \langle m!p.f=0 \rangle \langle m!l:1 \rangle \langle m!k:1 \rangle \langle s!l:2 \rangle \langle t!k:2 \rangle \\ & (m: \text{Main} \mid p: \text{Int} \mid s: S \mid t: T \mid \llbracket - \rrbracket) \end{aligned}$$

Given the semantics of `start`, there are synchronization edges from m to the threads that it creates, here manifest in the synchronization variables l and k , which will never be used again. Because of these synchronization edges, the write of the initial value of $p.f$ happens-before any actions performed by s or t , since these threads are part of the process filling the hole in the context.

Many concurrent algorithms use locks. When locks are implemented using atomics, the release of a lock happens-before the subsequent acquire.

In this paper we reason about happens-before relations using *correspondence assertions* [Woo and Lam 1992; Gordon and Jeffrey 2003]. Using this technique, a programmer may annotate a program with `begin` and `end` statements. The execution of an `end` statement is *correct* if it occurs *after* the corresponding `begin`. The primary goal of static analysis is to ensure that all ends are correct.

In a well-typed program, therefore, end statements can be erased without changing the meaning of the program.

Correspondences have no computational effect. The particular form of correspondence is chosen in order to specify some property of interest. We wish to reason about happens-before relations in code built using atomics and other mechanisms, and we wish to do so in a way that might be useful for API documentation.

Recall the example from `java.util.concurrent` quoted in the introduction: “Actions in a thread prior to placing an object into any concurrent collection happen-before actions subsequent to the access or removal of that element from the collection in another thread.” Directly capturing such statements requires dependent types, as well as a selection of analyses based on locality [Sewell 1998], confinement [Zhao, Palsberg, and Vitek 2006], linearity [Hawblitzel 2005], and effects [Lucassen and Gifford 1988]. Such powerful type systems provide strong guarantees at the cost of additional programmer effort that we do not wish to mandate.

Our goal has been to develop a type system that captures some property of happens-before while retaining simple types. To achieve this, we propose a rather weak notion of correspondence.

In our minimalist approach, the creation of an object `p` by a thread `s` begins a correspondence, creating a begin action $\langle s!p \rangle$. At any point, a program can attempt to end the correspondence using the statement `end p`. If $\langle s!p \rangle$ happens-before the hole in the context where `end p` occurs, then we say that this occurrence of `p` is *correctly published*, and the end acts as a no-op; otherwise the thread becomes *stuck*. For example, consider the following process.

$$\langle s!p \rangle (s[\text{end } p; \dots] \mid t[\text{end } p; \dots])$$

Thread `s` evaluates to $s[\dots]$ whereas thread `t` is stuck: the begin in context $\langle s!p \rangle [-]$ only happens-before the hole when the hole is filled by `s`. A stuck program is erroneous; typing ensures that well-typed programs cannot get stuck.

This form of specification is weak in the sense that a programmer cannot choose the point at which a begin occurs; begins always coincide with object creation. The programmer is also restricted to end on a single object reference, not a tuple.

Additionally, Java allows the null value wherever an object reference can occur, and we make no attempt to countermand this design decision. As in FJ [Igarashi, Pierce, and Wadler 2001], the definition of stuckness is tuned to the power of the type system, which cannot prevent null or casting errors. By definition, null errors and casting errors do not cause a *stuck* thread, simply an *irreducible* one. Stuckness is precisely defined in Section 6.2.

Nonetheless, our correspondences can be used to associate arbitrary program points by creating a dummy object to denote the begin statement, communicating that object, and then performed an end using the received value. The correspondence is only meaningful, however, if the received value is that which is expected. Since begins are performed on *every* object, we have factored out any data dependencies and therefore removed the need for dependent types.

To understand a concurrent data structure, one must understand both the data structure invariants and the memory effects. Our correspondence should be read as capturing the necessary memory effects under the supposition that the (informal) data structure invariants hold.

In general, shared fields do not preserve safety; that is, a thread may get stuck if it performs an end on a value read from a shared field, even if the writer could have performed the end. For example, consider the following process.

$$\langle s!o \rangle \langle s!p.f=o \rangle (s[\text{end } o;] \mid t[\text{val } x=p.f; \text{end } x;])$$

Thread `s` has created and written `o`. The residual $s[\text{end } o;]$ is safe, reducing to $s[]$; however the residual $t[\text{end } o;]$ is stuck.

Atomics do preserve safety. Consider the following program, derived from the previous one by replacing `p.f` with `atomic l`.

$$\langle s!o \rangle \langle s!l:1 \rangle (l:\text{Atomic}\{o;1\} \mid s[\text{end } o;] \mid t[\text{val } x=l.get(); \text{end } x;])$$

In this case, the residual $t[\text{end } o;]$ is executed in the context

$$\langle s!o \rangle \langle s!l:1 \rangle \langle t!l:2 \rangle [-]$$

and therefore is not stuck.

Example C1 from the introduction can be rewritten to use correspondences as follows, where `p:C1'`

```
class C1' { Object f; Object g; }
s[p.f=new Object(); p.g=new Object();] |
t[val y=p.g; val x=p.f;
  if(y!=null){end y; if(x==null){†}}]
```

In `C1'`, the end assertion can get stuck, since there is no happens-before edge from `s` to `t`.

Example C2 can also be written in this form, where `p:C2'`

```
class C2' { Object f; final Atomic g; }
s[p.f=new Object(); p.g.set(new Object());] |
t[val y=p.g.get(); val x=p.f;
  if(y!=null){end y; if(x==null){†}}]
```

In `C2'`, the end assertion cannot get stuck, and statement `†` is unreachable. If the reads of `t` occur in the reverse order, then `t` could become stuck and statement `†` could be unreachable.

3. Speculation

A substantial technical hurdle in proving soundness of any type system for the JMM is the treatment of speculation. We first describe our semantics for speculation, then discuss the implications for the proof. See our previous paper [Jagadeesan, Pitcher, and Riely 2010] for details.

While the JMM is clearly designed with type safety in mind, it does not lend itself to standard proofs using subject reduction. The JMM validates an execution using a sequence of *commitments* and a corresponding sequence of *validating executions*: a commitment C is a set of actions resolving some read-write data races; in a sequence of valid executions, execution E_i must justify C_i using C_{i-1} , subject to various coherence criteria. The coherence criteria for the JMM are notoriously subtle, and various revisions have been proposed [Cenciarelli, Knapp, and Sibilio 2007; Sevcík and Aspinal 2008; Sevcík 2008]. These coherence criteria complicate the normal inductive argument for soundness, perhaps accounting for the fact that no soundness proof has been published as yet (to the best of our knowledge).

For our soundness proof, we adopt the semantics proposed in our previous work, which handles speculation quite differently. Our semantics allows every execution allowed by the JMM, with the exception of some programs that contain both synchronization actions and data races. As a result of these subtle differences, our semantics validates many common program transformations that the JMM was meant to validate, but does not.

In addition to the mechanisms present in the previous section for fields and atomics, the semantics allows *single threaded action rewriting* and *speculation*.

Single threaded action rewriting is easy to understand. We allow dynamic transformations to the action sequence generated by a single thread, as long as this does not introduce new behaviors. For example, it is permitted to rewrite $\langle s!p.f=42 \rangle \langle s!p.f=27 \rangle$ to $\langle s!p.f=27 \rangle$, removing the value 42, which may be visible to concurrent threads. The converse transformation is not sound, however, since it introduces the value 42 out of thin-air.

Speculation is far more subtle. Let “ $\uparrow x$ ” stand for `return x` in the following example.

```
s[ val x=p.g; p.f=27;  $\uparrow x$  ] | t[ val y=p.f; p.g=y; ]
```

In any sequentially consistent execution, `s` must read the initial value 0 from `p.g`, resulting in `s[$\uparrow 0$]`. A speculative semantics also allows the result `s[$\uparrow 27$]`. In an implementation, such an execution may be the result of cache effects or optimizations that reorder the independent statements of `s`.

The basic idea of speculation is to allow `s` to read the value 27 from `p.g` before it is written by `t`. Clearly, we are playing with knives. It is common to impose two sanity checks on speculation. First, it should not create data out of “thin air”. Second, any program without data races should only have sequentially consistent executions. Our semantics guarantees both properties by restricting valid speculations. We refer to our previous paper for full details, providing only a flavor of the ideas here.

Speculation is not performed actively *by* a process. Rather, a speculation happens *to* a process. Speculation is performed, nondeterministically, by the reduction relation itself. For example, let `A` be the original pair of threads given above. We have the following “reduction,” which speculates that `t` will write 27 to `p.g`.

$$A \rightarrow ((\top \Rightarrow A) \parallel ((\uparrow ?p.g=27) \Rightarrow A))$$

Speculative “actions” are marked with a query (?), whereas concrete action are marked with a bang (!). The reduction creates two copies of the original process, which are executed in separate universes with separate copies of the state. The left copy is called the *initial* process; the right, the *final* process.

The initial and final processes evolve independently. Within the final process, any thread but `t` can read the speculative value. This allows reduction to the following.

$$\begin{aligned} & \top \Rightarrow \langle s!p.f=27 \rangle \langle t!p.g=27 \rangle (s[\uparrow 0] \mid t[]) \\ & \parallel \langle t?p.g=27 \rangle \Rightarrow \langle s!p.f=27 \rangle \langle t!p.g=27 \rangle (s[\uparrow 27] \mid t[]) \end{aligned}$$

Actions performed by both processes are allowed to trickle out of the speculation, in order. In addition, object denotations and threads can move in and out of the speculation, as long as there are identical copies on both sides. In the case of our example, this results in

$$\langle s!p.f=27 \rangle \langle t!p.g=27 \rangle \left(\parallel \langle t?p.g=27 \rangle \Rightarrow \left(\begin{array}{c} \top \Rightarrow (s[\uparrow 0] \mid t[]) \\ (s[\uparrow 27] \mid t[]) \end{array} \right) \right)$$

When a speculation occurs in a context with a matching concrete write, the speculation may be removed, leaving only the final process; the initial process is used only to justify the action that removes the speculation. This is called *finalization*. For our running example, the result is

$$\langle s!p.f=27 \rangle \langle t!p.g=27 \rangle (s[\uparrow 27] \mid t[]).$$

A *valid* execution is one in which every speculation can be finalized; non-valid executions may create values out of thin air, and, therefore, are not interesting. Nondeterminism ensures that the reduction relation captures all valid executions.

The soundness theorem establishes that typed programs cannot become stuck and therefore that every `end p` occurs in a context such that, for some `s`, `(s!p)` happens-before the context hole.

Soundness only applies to valid executions. The difficulty is to find an invariant that holds in the face of partial executions which have no valid extension. Such partial executions are caused by speculations which cannot be finalized, which we refer to as *traps*.

If a trap is the result of shape error, our type system can safely treat the final branch as untyped: we simply “turn off” typing in worlds that we know can never be exposed at top level.

Our type system includes effects, which formalize happens-before relations using correspondence assertions. Thus, in addition

to shape errors, there are also *subtle* traps, such as speculations that occur without a thread holding appropriate locks. Our proof addresses this subtlety by being safely optimistic about effects: if the optimism is justified, then it has done no harm; if the optimism is not justified, then the speculation must be a trap. In either case, subject reduction holds. For further discussion, see Section 7.1.

It may be argued that we should simply stop the reduction relation from generating unfinalizable speculations. In general, however, unfinalizability is undecidable. Of course, we can rule out certain classes of speculation which are clearly unfinalizable, including shape errors and locking errors. However, we believe that in order to know that a speculation is unfinalizable, you must allow the speculation to occur, and then *prove* that it cannot be finalized.

4. Static approximation of happens-before

Recall from Section 2 that an occurrence of an object reference is *correctly published* if an end statement using that occurrence cannot become stuck. An end statement that cannot become stuck in any execution is *safe*. We introduce typing annotations to statically approximate correct publication.

Correct publication is a predicate on *occurrences* of object references, not on objects themselves: threads may have different happens-before relations to the begin action that marks the creation of an object. A copy of a correctly published reference need not itself be correctly published. In particular, correct publication may be lost when a reference is copied via a shared object. Communication using an atomics preserves correct publication, since an atomic read is related by happens-before to every write to the same atomic. However, communication using a non-atomic does not necessarily preserve correct publication.

A field or variable annotated as *reliable* (notation \boxtimes) must hold only correctly published references. Annotations may be attached to any field or variable, including method formal parameters, the implicit parameter `this`, and return values. The key to safely approximating correct publication in the type system is to ban read-write data races on reliable fields. We consider three mechanisms for doing so: final fields, atomics and lock-protected fields. We discuss these three primitives in more detail below. In the following subsections, we discuss larger data structures using these primitives.

Finals fields. Final fields have a special semantics in the JMM and can be used to communicate correct publication. If field `f` is final and reliable, then `p.f` is correctly published if the occurrence of `p` is correctly published.

Atomics. We take `Atomic` to be primitive in the dynamics, and provide it an enriched signature in the statics to indicate that it preserves happens-before relations. We first present a simplified version of the signature for `Atomic`.

```
class Atomic {  $\boxtimes$  Object get();
               void set( $\boxtimes$  Object v); ... }
```

In the case of `get`, the reliable annotation indicates that a client program can safely end on the returned value. In the case of `set`, the annotation requires that all values written to an atomic be correctly published. This restriction may be undesirable in some cases, and therefore we introduce polymorphism over annotations. The final form of the atomic interface is as follows.

```
class Atomic( $\chi$ ) {  $\chi$  Object get( $\square$ this);
                 void set( $\square$ this;  $\chi$  Object v); ... }
```

Here χ is an annotation variable, which may be instantiated to \boxtimes or \square , indicating whether a variable is reliable. Reliable variables must hold only correctly published values, whereas variables that are not reliable variables may hold any value.

It is always safe to forget that a value is correctly published, thus losing the capability to end. Annotations therefore give rise to a natural form of subtyping with $\boxtimes <: \chi <: \square$.

In general, annotations are required on the implicit parameter `this`. Here the annotations indicate that correctly published values may be recovered from a `Atomic` that is not itself correctly published, that is, `this` is marked \square .

We elide \square annotations except when they occur as actual parameters to generic classes. We also elide the `this` annotation in method declarations when it is \square `this`.

To typecheck example C2' from the end of Section 2 in our system, we must specify that the atomic held in `g` is reliable. The correct declaration of C2' is as follows.

```
class C2'' { Object f; final Atomic( $\boxtimes$ ) g; }
s[p.f=new Object(); p.g.set(new Object());] |
t[ $\square$ val y=p.g.get();  $\square$ val x=p.f;
  if(y!=null){end y; if(x==null){ $\dagger$ }}
```

In this example, there are two classes of executions: those in which `t` gets null from `p.g`, and those in which `t` gets the object set by `s`. In the former case, the annotations on `Atomic` say nothing. In the latter case, the annotations indicate that the creation of the parameter to set happens-before the return of `get`. That is, happens-before relations are preserved by `Atomic` from a set to the corresponding `get`.

Although we take `Atomic` to be a primitive, one can imagine varying implementations. Implementing `Atomic` by simply reading and writing a field would violate the interface given above, since such an implementation would allow a write before a set to race against a read after the corresponding `get`. However, an implementation that always returns null would satisfy the interface trivially. More subtly, so would an implementation that created a fresh object for each `get`, or always returned the first object passed to `set`. In both cases, the happens-before relation that results is not the expected one. In the former case, we have that the body of `get` happens-before the return of `get`. In the latter case, we have that the first call to `set` happens-before the return of every subsequent call to `get`. In short, the annotations say nothing about behavior of `Atomic` as a *data structure*. As a data structure, `Atomic` requires that after the first set, every `get` action returns the value from the “previous” set. Such requirements are readily understood informally, but difficult to formalize in a way that appeals to nonspecialists.

Our goal has been to define a typing system that captures interesting properties of happens-before without commenting on data structure invariants. Our solution is to leave the notion of “corresponding actions” in the dynamics, using object identity. The resulting type system can be used alone or as a module in a larger system with additional tools to establish data structure invariants and confinement properties. Used alone, the statics provide conditional guarantees of properties of interest, such as the reachability of statement \dagger . If values match up and the necessary confinement properties hold then the properties of interest can be inferred from the happens-before relation.

Lock-protected fields. Communicating correct publication on fields which are neither final nor atomic is rather more delicate, as one must guarantee the absence of read-write data races programmatically. The use of locks to avoid races has been studied by Abadi, Flanagan, and Freund [2006]. We adapt the simplest of their systems to our setting by allowing write-write data races on all fields and read-write races on fields that are not reliable. Consider the following locking variant, where `p:C3` and `l:Lock`. (Here synchronized statements do not create scopes, and therefore `y` remains visible.)

```
class C3 { Object f;  $\boxtimes$ Object g guardedby l; }
s[p.f=new Object();
  synchronized(l){p.g=new Object();}] |
t[synchronized(l){ $\square$ val y=p.g;  $\square$ val x=p.f;
  if(y!=null){end y; if(x==null){ $\dagger$ }}
```

Just as in example C2' above, the end assertion is safe, and statement \dagger is unreachable. In this case, the happens-before relation is carried on lock `l`. The type of `g` indicates that it is a conservative field guarded by `l`. A guarded field may only be accessed in a context that holds the corresponding lock, thus ensuring the necessary happens-before relations from lock acquisition to field read and from field write and lock release.

In general, we allow reads and writes to be guarded by different boolean combinations of locks, requiring that any set of locks satisfying the read guard must *overlap* with every set of locks satisfying the write guard. (Overlap is defined in Section 5.2 to capture the intuition of non-disjoint intersection.) This disallows read-write data races while still allowing write-write data races. Consider an example from Sevcik [2008] with write-write data races, but no read-write data races, where `p:D`, `l:Lock`, and `k:Lock` are shared.

```
class D {  $\boxtimes$ Object f rdguard (l&& k) wrguard (l || k); }
s[synchronized (l){p.f=new Object();}] |
t[synchronized (k){p.f=new Object();}] |
u[synchronized (l){
  synchronized (k){ $\square$ Object x=p.f;}}
```

The annotations indicate that the read of `p.f` requires both locks `l` and `k`, whereas the write of `p.f` requires only one of them. This program typechecks in our system. While this example is contrived, the separate treatment of read and write capabilities on objects is critical to the design of efficient data structures to avoid unnecessary sequentialization (see Java’s concurrent hash tables).

4.1 Dummy variables

Our technique uses object identity to reason about dynamic occurrences of program points via correspondence assertions. Occasionally, however, interfaces lack appropriate variables to annotate the happens-before relations of interest. In such cases, we require that the interface be augmented with *dummy variables*, which can be systematically erased at runtime. For example, consider the following methods of `Atomic`, with candidate annotations. (Recall that we elide the annotation for the implicit parameter `this` from method types when annotation is \square .)

```
class Atomic( $\chi$ ) { ...
   $\chi$  Object getAndSet( $\chi$  Object v);
  boolean compareAndSet(Object oldv,  $\chi$  Object newv); }
```

Using the data structure invariants, the annotation of the `getAndSet` method captures exactly the right property: a return from `getAndSet` happens-after the previous set, and a call to `getAndSet` happens-before the subsequent `get`. The `compareAndSet` method has the same memory effect as `getAndSet`, however, its annotation only gives half of the story: the return value is unannotated. The situation can be remedied using a dummy variable in the signature of `compareAndSet`.

```
Pair(boolean,  $\chi$  Object)
  compareAndSet(Object oldv,  $\chi$  Object newv);
```

If `compareAndSet` returns false, the dummy variable returns null; otherwise, the dummy variable returns the previous value stored by the atomic, as in `getAndSet`.

The unannotated interface of `BinaryLatch` (adapting Java’s `CountDownLatch`) is:

```
class BinaryLatch { void await(); void countDown(); }
```

The documented memory effect is that a return of `await` happens-after the corresponding call to `countDown`. Applying our methodology leads to the interface

```
class BinaryLatch( $\chi$ ) {  $\chi$  Object await();
                      void countDown( $\chi$  Object v); }
```

where `await` returns the value passed to the last call to `countDown`. Similar changes can be made systematically throughout the APIs.

```
class Condition( $\chi$ ) {  $\chi$  Object await();
                   void signal( $\chi$  Object); }
class Semaphore( $\chi$ ) {  $\chi$  Object acquire();
                   void release( $\chi$  Object v); }
```

The annotations on `Condition` indicate the memory effect that a return of `await` happens-after the corresponding call to `signal`, and similarly for the (binary) `Semaphore`.

4.2 Building data structures

From the primitive constructs with special memory effects—final fields, atomic fields, and lock-protected fields—it is possible to construct useful concurrent data structures. The happens-before relations created by these data structures are key to concurrent programming. Indeed, *every* data structure provided by the `java.util.concurrent` API specifies such memory consistency effects. Our typing annotations are a lightweight mechanism for making these memory effects amenable to the static analysis.

For example consider `BlockingQueue` from `java.util.concurrent`. The Java APIs specify the behavior of a `BlockingQueue` in two parts.

- The *data structure* specification states that there is a one-to-one correspondence between invocations of the `take` and the `put` methods, such that a `take` returns the `Object` that is added into the data structure by its *corresponding* `put`.
- The *memory consistency* specification states that “actions in a thread prior to placing an object into a `BlockingQueue` happen-before actions subsequent to the access or removal of that element from the `BlockingQueue` in another thread”.

In our system, the `BlockingQueue` is annotated as follows.

```
class BlockingQueue( $\chi$ ) { void put( $\chi$  Object o);
                        $\chi$  Object take(); ... }
```

As with `Atomic`, the annotations do not capture the data structure invariant, and only very roughly approximate the memory consistency specification. Nevertheless, they provide valuable information. Consider the following variant of our running example, where `p:C4` and `q:BlockingQueue(\boxtimes)`.

```
class C4 { Object f; Object g; }
s[p.f=new Object(); p.g=new Object();
  q.put(new Object());] |
t[ $\text{val } z=q.\text{take}(); \text{val } y=p.g; \text{val } x=p.f;
  \text{end } z; \text{if}((y!=\text{null})\&\&(x==\text{null}))\{\dagger\}]$ 
```

The call to `take` blocks until `put` begins execution. Assuming that `q` is initially empty and that there are no other threads, then `t` must take the object that is put by `s`. In this case, the \boxtimes annotation tells us that the call to `put` happens-before the return of `get`. By thread-local reasoning, we can conclude that the writes by `s` must happen-before the call to `put`, and also that the reads by `t` must happen-after the return of `take`. Reasoning by transitivity, we can conclude that statement \dagger is unreachable.

Formally, our system allows the inclusion of “end `z`” in thread `t`, indicating that the call to `put` happened-before the return of `take`. To reason about the reachability of \dagger , one must also reason about thread-local happens-before relations and the confinement `q`

and `p`. Our system captures the crucial *cross-thread* reasoning required about happens-before, leaving the local reasoning to informal methods or modular formal methods.

In the rest of this section, we present both non-blocking and lock-based examples which typecheck in our system, providing typed implementations of the interfaces discussed above.

4.3 Non-blocking algorithms

The basic building block for writing non-blocking algorithms is the `Atomic` class. For example, one can implement the `BinaryLatch` interface using an `Atomic`.

```
class BinaryLatch( $\chi$ ) {
  final Object init;
  final Atomic( $\chi$ ) witness = new Atomic();
  BinaryLatch() {
    witness.set(new Object());
    init = witness.get(); }
   $\chi$  Object await() {
    while (witness.get() == init) ;
    return witness.get(); }
  void countDown( $\chi$  Object o) {
    witness.compareAndSet(init, o); } }
```

Informally, one can reason that the return of `await` must happen-after the call to `compareAndSet` which must happen-after the corresponding call to `countDown`. Thus, the implementation validates the interface of `BinaryLatch`.

This guarantee is established formally by typechecking the code in our system. The type system uses the `Atomic` interface to deduce that the return value of `await` may be annotated by χ . The body of `countDown` typechecks because the second argument of `compareAndSet` carries annotation χ . The set of the witness in the constructor typechecks because newly created references are viewed as initially created with a \boxtimes annotation.

Our type system can validate Treiber’s algorithm for concurrent stacks, from [Goetz, Peierls, Bloch, Bowbeer, Holmes, and Lea 2005], with the following interface.

```
class ConcurrentStack( $\chi$ ) {  $\chi$  Object pop();
                          void push( $\chi$  Object v); }
```

When `ConcurrentStack(\boxtimes)` satisfies the usual data structure invariant, the absence of a write-read data race on the \boxtimes reference transmitted from the `push` to the `pop` establishes the required happens-before edge between a return of `pop` and the corresponding call of `push`. This annotated interface is validated by the following implementation. (We elide the dummy return value of `compareAndSet`.)

```
class Node( $\chi$ ) {
  final  $\chi$  Object item;
  final  $\boxtimes$  Node( $\chi$ ) next;
  Node( $\chi$  Object item,  $\boxtimes$  Node( $\chi$ ) next) {
    this.item = item; this.next = next; } }
class ConcurrentStack( $\chi$ ) {
  final Atomic( $\boxtimes$ ) top = new Atomic( $\boxtimes$ )();
  void push( $\chi$  Object item) {
     $\boxtimes$  Node( $\chi$ ) oldHead;
     $\boxtimes$  Node( $\chi$ ) newHead;
    do { oldHead = top.get();
        newHead = new Node(item, oldHead);
    } while (!top.compareAndSet(oldHead, newHead)); }
   $\chi$  Object pop() {
     $\boxtimes$  Node( $\chi$ ) oldHead;
     $\boxtimes$  Node( $\chi$ ) newHead;
    do { oldHead = top.get();
```

```

    if (oldHead == null) return null;
    newHead = oldHead.next;
  } while (!top.compareAndSet(oldHead, newHead));
  return oldHead.item; } }

```

The annotation parameter of the `ConcurrentStack` (and `Node`) classes matches that on the items that are stored in the stack. On the other hand, the code maintains the invariant that all references to the nodes on the stack carry \boxtimes annotation. This invariant is used to satisfy the requirement that the second arguments of the `compareAndSet` carry \boxtimes annotation. In order to maintain the invariant at the creation of new stack nodes in the `push` method, the `Node.next` field must be final.

4.4 Programs with locks

Our locks are based on Java locks, with some different conventions to simplify the semantics. Synchronization can only be performed using block-based synchronization on an instance of the `Lock` class. We take both `Lock` and the associated `Condition` class to be primitives; however, one can safely think of these as being implemented using atomic objects.

Locks can be used to validate the following implementation of the enriched interface for binary semaphores discussed above.

```

class Semaphore( $\chi$ ) {
  final Lock l = new ReentrantLock();
  final Condition( $\chi$ ) open = new Condition(l);
   $\chi$  Object witness guardedby l;
   $\chi$  Object acquire() {
     $\chi$  Object temp;
    synchronized(l) {
      while (witness == null) {
        witness = open.await(); }
      temp = witness;
      witness = null; }
    return temp; }
  void release( $\chi$  Object o) {
    synchronized(l) {
      if (witness == null) {
        witness = o; open.signal(o); } } }

```

The required guarantee follows from typing. The `witness` field is guarded by lock `l`, and our system ensures that all accesses to `witness` happen only when this lock has been acquired. The assignment to `witness` in `acquire` typechecks because of the `Condition` interface. In typechecking `countDown`, the `Condition` interface forces the argument of `release` to have χ annotation. The two cases of control flow for the witness returned by `acquire` are merged via the annotations on `witness` and `temp` and their typechecking.

5. Typing classes

We present the syntax and type rules for classes, using the annotations described in the previous section. In the next section, we describe the safety theorem: starting from a *well-typed* class table, (small step) *evaluation* of the *bootstrap process* cannot get *stuck*, and therefore, in particular, every `end` must always happen-after the creation of the corresponding object.

The statement of safety requires that we define well-typed classes, the evaluation relation, the bootstrap process and the stuck predicate. The proof of safety follows by defining well-typed *processes*, demonstrating that the bootstrap process is well-typed, and proving preservation and progress.

To simplify the semantics, we use a more restrictive language than that of the previous section. These changes are made only for conciseness of exposition: they do not alter the expressive power

of the language or its type system. We make all local variables immutable and implement iteration via recursion. We make all sequencing explicit; for example `return this.f.g` must be written `val x=this.f; return x.g`.

The operational semantics follows our previous paper [Jagadeesan, Pitcher, and Riely 2010], which is in turn based on the JMM. In order to capture the properties of interest, we have enriched our previous semantics to include final fields, as well as primitive support for the `Atomic` and `Condition` classes. Unlike our previous work, we require block-structured use of `Locks`, which simplifies typing. Since local variables are immutable, we add a return value to these synchronization blocks, so that they can provide results to the thread without requiring the use of a mutable field.

5.1 Syntax

Let x and y range over variable names (including the reserved variable `this`), d over class names (including the reserved classes `Object`, `Atomic`, `Lock` and `Cond`), f and g over field names, and m over method names (including the reserved methods `start` and `run`).

Class types may be parameterized by annotations. Let χ range over annotation variables and γ range over the ground annotations ($\gamma ::= \boxtimes | \square$). An *annotation*, a, b, c , is either an annotation variable or a ground annotation ($a, b, c ::= \chi | \gamma$). Class types, D , have the form $d(\vec{a})$. A type may be a class type or a base type ($T, S ::= D | bt$). We routinely drop empty type brackets $\langle \rangle$.

Let bv range over base values (including integers and the constants `unit`, `true` and `false`). The set of ground values, v, w, u , includes `null` and the base values. (For running processes, the set of ground values will also include object names.) Open values include ground values and variables ($V, W, U ::= v | x$). Let op range over base value operators (such as `==`, `+`, `&&`). We assume that `null` has meaning at all base types (zero for integers, false for booleans). The statement language is as follows:

$M, N ::= \text{return } V;$	(Return statement)
$\text{val } x = \text{new } D(\vec{V}); M$	(Creation statement)
$\text{val } x = W.m(\vec{V}); M$	(Method statement)
$\text{val } x = op(\vec{V}); M$	(Operator statement)
$\text{val } x = (D)V; M$	(Cast statement)
$\text{end } V; M$	(End statement)
$\text{val } x = V.f; M$	(Field read statement)
$V.f = W; M$	(Field write statement)
$\text{if } (V) \{M\} \text{ else } \{N\}$	(Conditional statement)
$\text{val } x = \text{sync } V \{N\} M$	(Synchronization statement)

As discussed above, synchronization statements allow a return value. All the other constructs are standard.

We often drop the keyword `val`, use complex expressions and infix notation for operators and drop instances of “`return unit`”; Thus, “`y = a+b+c`,” is sugar for “`val x = +(a,b); val y = +(x,c); return unit`,” where x is fresh. We write “`val x = \dots ; M`” as “ $\dots; M$ ” if x does not occur free in M . We write $\uparrow V$ for “`return V`,” and $\uparrow(V, W)$ for “`val x = new Pair(V, W); return x`,” where x is fresh. We write “`if (V) {val x = \dots ; } M`” for “`if (V) {val x = \dots ; M} else {M}`” if x does not occur free in M ; this notation extends to field write statements, conditional statements and sequences of statements in the obvious way.

The variable x is bound with scope M in all statements of the form “`val x = \dots ; M`”. For any syntax category, let fv return the set of free variables and let fn return the set of free names.

Class definitions have the following form.

$\mathcal{D} ::= \text{class } d(\vec{\chi}) \{ \vec{\mathcal{F}}. \vec{\mathcal{M}} \}$	(Class declaration)
$\mathcal{M} ::= aT m(b\text{this}; \vec{a}\vec{T} \vec{x}) \{M\}$	(Method declaration)
$\mathcal{F} ::= Tf; \text{final } aDf;$	(Field declaration)

$$\begin{aligned}
& | aDf \text{ rdguard } \Phi \text{ wrguard } \Psi; \\
\Phi, \Psi & ::= \text{true} \mid F \mid \Phi \&\& \Psi \mid \Phi \parallel \Psi & (\text{Guard}) \\
F, G & ::= V \mid F.f & (\text{Field path})
\end{aligned}$$

There are field declaration forms for normal, final and lock-protected fields. Annotated fields must have object types. Annotated base types are useless since `end` requires an object type. Lock-protected fields have read and write guards, which are boolean combinations of field paths. When typing classes, all field paths will have the form `this.f1.f2...fn`. We elide read or write guards when uninteresting and write `guardedby` Φ as shorthand for `rdguard` Φ `wrguard` Φ . We elide the \square annotation in variable and field declarations, and \square `this` in method declarations.

The annotation variables $\vec{\chi}$ are bound in the declaration `class` $d(\vec{\chi})\{\vec{\mathcal{F}}\}\vec{\mathcal{M}}$, with scope $\vec{\mathcal{F}}$ and $\vec{\mathcal{M}}$. We identify syntax up to renaming and write $\vec{\mathcal{F}}\{\vec{a}/\vec{\chi}\}$ for the capture avoiding substitution of \vec{a} for $\vec{\chi}$ in $\vec{\mathcal{F}}$, and likewise for $\vec{\mathcal{M}}$.

Suppose d is declared as `class` $d(\vec{\chi})\{\vec{\mathcal{F}}\}\vec{\mathcal{M}}$. Define the partial function $f\text{type}$ so that $f\text{type}(d(\vec{a}).f) = \text{final } aT$ if $\vec{\mathcal{F}}\{\vec{a}/\vec{\chi}\}$ contains the declaration “`final` $aT f$,” and likewise for racing and lock-protected fields. Define the partial function $m\text{type}$ so that $m\text{type}(d(\vec{a}).m) = b;\vec{a}\vec{T} \rightarrow aT$ if $\vec{\mathcal{M}}\{\vec{a}/\vec{\chi}\}$ contain the declaration $aT m(b\text{this};\vec{a}\vec{T} \vec{x})\{M\}$. Define $\text{finals}(D) = \vec{f}$ if \vec{f} are the final fields of class D , and similarly nonfinals .

5.2 Typing

Typing environments have the following form.

$$E ::= E, \chi \mid E, x : aT \mid E, \text{final } x = F \mid E, \text{lock } V$$

We identify environments up to reordering and repetition. Supporting judgments such as well-formed environment ($E \vdash \diamond$), type ($E \vdash T$), and annotation ($E \vdash a$) simply account for free and bound annotation variables. These definitions are elided.

Guard satisfaction is a propositional logic over field paths, with substitution of final paths.

$$\begin{array}{c}
\frac{E \ni \text{lock } F \quad E, E' \{\{F/x\}\} \Vdash \Phi}{E \Vdash F} \\
\frac{E \Vdash \Phi \quad E \Vdash \Psi \quad E \Vdash \Phi \quad E \Vdash \Psi}{E \Vdash \text{true}} \quad \frac{E \Vdash \Phi \&\& \Psi}{E \Vdash \Phi \&\& \Psi} \quad \frac{E \Vdash \Phi \parallel \Psi}{E \Vdash \Phi \parallel \Psi} \quad \frac{E \Vdash \Psi}{E \Vdash \Phi \parallel \Psi}
\end{array}$$

Intuitively, two guards *must overlap* if the sets of field paths that satisfy them must always have a non-disjoint intersection. The formal definition is a safe syntactic approximation of this intuition: Let \mathbb{F} and \mathbb{G} range over sets of field paths. Then any guard Φ can be written in disjunctive normal as $\bigvee_i \mathbb{F}_i$, where each set \mathbb{F}_i is viewed as a conjunction of field paths. We say that Φ *must overlap with* Ψ if $\Phi = \bigvee_i \mathbb{F}_i$, $\Psi = \bigvee_j \mathbb{G}_j$, and $\mathbb{F}_i \cap \mathbb{G}_j \neq \emptyset$ for all i, j .

Class ($\vdash \mathcal{D}$), method ($E \vdash \mathcal{M}$ in D) and field declarations ($E \vdash \mathcal{F}$) are as follows.

$$\begin{array}{c}
\frac{\forall i. \vec{\chi} \vdash \mathcal{F}_i \quad \forall j. \vec{\chi} \vdash \mathcal{M}_j \text{ in } d(\vec{\chi})}{\vdash \text{class } d(\vec{\chi})\{\vec{\mathcal{F}}\}\vec{\mathcal{M}}} \\
\frac{E, \text{this} : bD, \vec{x} : \vec{a}\vec{T} \vdash M : aT}{E \vdash aT m(b\text{this};\vec{a}\vec{T} \vec{x})\{M\} \text{ in } D} \\
\frac{E \vdash T}{E \vdash Tf}; \quad \frac{E \vdash a \quad E \vdash D}{E \vdash \text{final } aDf}; \\
\frac{E \vdash a \quad E \vdash D \quad E \vdash \Phi, \Psi \quad \Phi \text{ must overlap with } \Psi}{E \vdash aDf \text{ rdguard } \Phi \text{ wrguard } \Psi};
\end{array}$$

These are standard definitions, with annotations added. There are three forms of field declaration, and correspondingly three type rules. The read and write guards of a lock-protected field must overlap.

$$\begin{array}{c}
\begin{array}{c}
(\text{VAL-VARIABLE}) \\
\frac{E \vdash \diamond \quad E \ni x : aT}{E \vdash x : aT}
\end{array} \quad \begin{array}{c}
(\text{VAL-NULL}) \\
\frac{E \vdash \diamond \quad E \vdash a \quad E \vdash D}{E \vdash \text{null} : aD}
\end{array} \\
\begin{array}{c}
(\text{VAL-SUB-TYPE}) \\
\frac{E \vdash V : aD}{E \vdash V : aObject}
\end{array} \quad \begin{array}{c}
(\text{VAL-SUB-ANN1}) \\
\frac{E \vdash V : aT}{E \vdash V : \square T}
\end{array} \quad \begin{array}{c}
(\text{VAL-SUB-ANN2}) \\
\frac{E \vdash V : \boxtimes T}{E \vdash V : aT}
\end{array} \\
\begin{array}{c}
(\text{STAT-NEW}) \\
\frac{\text{finals}(D) = \vec{f} \quad E \vdash \vec{V} : \vec{b}\vec{S} \quad \text{ftype}(D.f) = \text{final } \vec{b}\vec{S} \quad E, x : \boxtimes D \vdash M : aT}{E \vdash \text{val } x = \text{new } D(V); M : aT}
\end{array} \quad \begin{array}{c}
(\text{STAT-METHOD}) \\
\frac{E \vdash W : cD \quad E \vdash \vec{V} : \vec{b}\vec{S} \quad m\text{type}(D.m) = c; \vec{b}\vec{S} \rightarrow bS \quad E, x : bS \vdash M : aT}{E \vdash \text{val } x = W.m(\vec{V}); M : aT}
\end{array} \\
\begin{array}{c}
(\text{STAT-LOCK}) \\
\frac{E \vdash V : \square \text{Lock} \quad E, \text{lock } V \vdash N : bS \quad E, x : bS \vdash M : aT}{E \vdash \text{val } x = \text{sync } V \{N\} M : aT}
\end{array} \quad \begin{array}{c}
(\text{STAT-RETURN}) \\
\frac{E \vdash V : aT}{E \vdash \uparrow V : aT}
\end{array} \\
\begin{array}{c}
(\text{STAT-FINAL-READ}) \\
\frac{E \vdash V : cD \quad \text{ftype}(D.f) = \text{final } bS \quad E, x : b'S, \text{final } x = V.f \vdash M : aT}{E \vdash \text{val } x = V.f; M : aT}
\end{array} \quad b' = \begin{cases} b, & \text{if } b = c \\ \square, & \text{otherwise} \end{cases} \\
\begin{array}{c}
(\text{STAT-RACING-READ}) \\
\frac{E \vdash V : \square D \quad \text{ftype}(D.f) = S \quad E, x : \square S \vdash M : aT}{E \vdash \text{val } x = V.f; M : aT}
\end{array} \quad \begin{array}{c}
(\text{STAT-RACING-WRITE}) \\
\frac{E \vdash V : \square D \quad \text{ftype}(D.f) = S \quad E \vdash W : \square S \quad E \vdash M : aT}{E \vdash V.f = W; M : aT}
\end{array} \\
\begin{array}{c}
(\text{STAT-GUARDED-READ}) \\
\frac{\text{ftype}(D.f) = bS \text{ rdguard } \Phi \quad E \vdash V : \square D \quad E \Vdash \Phi \{\{V/\text{this}\}\} \quad E, x : bS \vdash M : aT}{E \vdash \text{val } x = V.f; M : aT}
\end{array} \quad \begin{array}{c}
(\text{STAT-GUARDED-WRITE}) \\
\frac{\text{ftype}(D.f) = bS \text{ wrguard } \Psi \quad E \vdash V : \square D \quad E \Vdash \Psi \{\{V/\text{this}\}\} \quad E \vdash W : bS \quad E \vdash M : aT}{E \vdash V.f = W; M : aT}
\end{array} \\
\begin{array}{c}
(\text{STAT-END}) \\
\frac{E \vdash V : \boxtimes D \quad E \vdash M : aT}{E \vdash \text{end } V; M : aT}
\end{array} \quad \begin{array}{c}
(\text{STAT-IF}) \\
\frac{E \vdash V : \square \text{boolean} \quad E \vdash M : aT \quad E \vdash N : aT}{E \vdash \text{if } (V) \{M\} \text{ else } \{N\} : aT}
\end{array}
\end{array}$$

Figure 1. Statement typing (rules for base types elided)

The judgments for well-typed values ($E \vdash V : aT$) and statements ($E \vdash M : aT$) are given in Figure 1; we have elided standard rules for base values, operators casting and conditionals.

STAT-NEW creates objects with \boxtimes . The final fields of the class must be initialized by the constructor; non-final fields are set to null. STAT-END requires \boxtimes objects.

VAL-SUB-ANN1 and VAL-SUB-ANN2 define subtyping on annotations, with \boxtimes as bottom and \square as top.

In STAT-LOCK, statement N is typed under the assumption that lock V has been acquired, allowing guard satisfaction in STAT-GUARDED-READ and STAT-GUARDED-WRITE.

For final fields, the annotation on V must agree with that of the field to impart any benefit to the reader; otherwise, the reader sees \square . The rules for racing fields impose no annotation restrictions on the writer, nor do they impart any annotation benefit to the reader. The rules for accessing a lock-protected field $V.f$ require that the field and value agree in their annotation; the annotation on V is irrelevant.

6. Soundness

To validate the soundness of our system, we use a small-step reduction relation. In this section, we explain the operational semantics, define soundness, and discuss highlights of the soundness proof.

There is insufficient space to include all definitions with sufficient motivation, therefore we elide the following (a) synchronization statements (the interesting issues are already revealed by atomics); (b) discussion of auxiliary definitions used in the operational

semantics; (c) most of the additional typing rules used to prove subject reduction; and (d) all proofs. These details are included in the full version of the paper.

6.1 Operational semantics

Let p, q, s, t and ℓ range over object names; by convention, we use name metavariables s, t for thread objects and ℓ for atomic objects. Process syntax is as follows, where name p is bound with scope A in the process $(vp)A$.

$$\begin{aligned}
v, w, u &::= \text{null} \mid bv \mid p && \text{(Ground value)} \\
M, N &::= \dots \mid \text{val } x = \{N\} M && \text{(Statement)} \\
\alpha, \beta &::= \langle s!p \rangle \mid \langle s!p.f=v \rangle \mid \langle s!\ell:j \rangle && \text{(Action)} \\
A, B &::= p:D \mid \ell:\text{Atomic}\{v; j\} && \text{(Process)} \\
& \mid \text{free } p \mid \text{runnable } p \\
& \mid \alpha A \mid s[M] \mid A \mid B \mid (vp)A \\
& \mid \top \Rightarrow A \parallel \langle s?p.f=v \rangle \Rightarrow B
\end{aligned}$$

The syntax of ground values and statements are extended to include the necessary runtime constructs. A partially executed method is written “ $\text{val } x = \{N\} M$ ”, where N is the (residual of the) method body, and M is the (residual of the) calling context; that is, M is the statement that executes after the method returns.

An action indicates that the creation of an object, a write, or a synchronization has occurred. As a process executes, actions accumulate in the context of a thread. The first two process forms provide denotations for object names. The denotation of an atomic includes the state of the object. The denotation of other objects includes only the class; the values of fields are determined by the action context, which varies over time and between threads. Lifecycle flags ($\text{free } p$ and $\text{runnable } p$) are used to track the lifecycle of an object. Object denotations, lifecycle flags and threads ($s[M]$) are composed using restriction ($(vp)A$), action prefixing (αA), parallel composition ($A \mid B$) and speculation ($\top \Rightarrow A \parallel \langle s?p.f=v \rangle \Rightarrow B$).

In the rest of this subsection, we formalize the reduction relation ($A \rightarrow B$). The exposition is necessarily dense, as we do not have space to provide motivation or discussion, which can be found in [Jagadeesan, Pitcher, and Riely 2010]. On first reading, it is best to skim this material, coming back to it as necessary.

Actions include empirical actions α, β , speculative actions ($\langle s?p.f=v \rangle$), ψ , and thread-labeled context holes $s[-]$. Let $\text{thrd}(\sigma)$ return the unique thread associated with an action. For write and speculation actions define loc and val to return the location and value of the action as $\text{loc}(\langle s!p.f=v \rangle) = \text{loc}(\langle s?p.f=v \rangle) = p.f$, and $\text{val}(\langle s!p.f=v \rangle) = \text{val}(\langle s?p.f=v \rangle) = v$. Let $\text{act}(\mathbb{C}, s)$ return the sequence of labeled actions occurring before the hole in \mathbb{C} .

$$\begin{aligned}
\text{act}([-], s) &= s[-] \\
\text{act}(\alpha \mathbb{C}, s) &= \alpha \text{act}(\mathbb{C}, s) & \text{act}(\mathbb{C} \mid A, s) &= \text{act}(\mathbb{C}, s) \\
\text{act}((vq)\mathbb{C}, s) &= \text{act}(\mathbb{C}, s) & \text{act}(A \mid \mathbb{C}, s) &= \text{act}(\mathbb{C}, s) \\
\text{act}(\top \Rightarrow A \parallel \langle s?p.f=v \rangle \Rightarrow \mathbb{C}, s) &= \langle s?p.f=v \rangle \text{act}(\mathbb{C}, s) \\
\text{act}(\top \Rightarrow \mathbb{C} \parallel \langle s?p.f=v \rangle \Rightarrow A, s) &= \text{act}(\mathbb{C}, s)
\end{aligned}$$

We say that context \mathbb{C} enables $\langle s!\ell:j \rangle$ if $j = 0$ or $\langle t!\ell:j-1 \rangle \in \text{act}(\mathbb{C}, s)$, for some t . We say that \mathbb{C}, s is contiguous if whenever $\mathbb{C} = \mathbb{C}' \parallel \langle t!\ell:j \rangle \mathbb{C}''$ for $j > 1$ then there exists t' such that $\langle t'!\ell:j-1 \rangle \in \text{act}(\mathbb{C}', s)$. We define several notions for contiguous contexts. Suppose \mathbb{C}, s is contiguous.

Define program order ($<_{po}^{\mathbb{C}, s}$) and synchronizes-with ($<_{sw}^{\mathbb{C}, s}$) as relations on the indices of $\vec{\sigma}$, where $\vec{\sigma} = \text{act}(\mathbb{C}, s)$.

$$\begin{aligned}
i <_{po}^{\mathbb{C}, s} j &\text{ iff } i < j \text{ and } \text{thrd}(\sigma_i) = \text{thrd}(\sigma_j) \\
i <_{sw}^{\mathbb{C}, s} j &\text{ iff } \sigma_i = \langle s!\ell:n-1 \rangle \text{ and } \sigma_j = \langle t!\ell:n \rangle \\
&\text{ for some } s, t, \ell \text{ and even } n
\end{aligned}$$

Define happens-before order ($<_{hb}^{\mathbb{C}, s}$) to be the transitive closure of the union of program order and synchronizes-with.

Suppose \mathbb{C}, s is contiguous. Let $\vec{\sigma} = \text{act}(\mathbb{C}, s)$. Let k be the index of $s[-]$ in $\vec{\sigma}$.

- Define \mathbb{C}, s justifies end p if there exists i and t such that $\langle t!p \rangle = \sigma_i$ and $i <_{hb}^{\mathbb{C}, s} k$
- Define \mathbb{C}, s justifies read $p.f=v$ if all of the following hold for some i .
 - $\sigma_i = \langle t!p.f=v \rangle$ for some t (possibly equal to s), or $\sigma_i = \langle t?p.f=v \rangle$ for some $t \neq s$; and
 - for every j such that σ_j is a write action and $i <_{hb}^{\mathbb{C}, s} j <_{hb}^{\mathbb{C}, s} k$, we have that $\text{loc}(\sigma_j) \neq \text{loc}(\sigma_i)$.
- Define \mathbb{C}, s justifies speculation $p.f=v$ if all of the following hold for some i .
 - $\sigma_i = \langle s!p.f=v \rangle$;
 - for every j such that σ_j is a write action and $i <_{hb}^{\mathbb{C}, s} j <_{hb}^{\mathbb{C}, s} k$, we have that $\text{loc}(\sigma_j) \neq \text{loc}(\sigma_i)$; and
 - for every j such that σ_j is a release action and $i <_{hb}^{\mathbb{C}, s} j <_{hb}^{\mathbb{C}, s} k$, we have that $\text{thrd}(\sigma_j) \neq \text{thrd}(\sigma_i)$.

Define the structural order $A \geq B$ to be the least precongurence on processes that satisfies the axioms in Equation 6.1 (where $A \equiv B$ abbreviates the two axioms $A \geq B$ and $B \geq A$). Define \equiv to be the kernel of \geq .

As usual, the structural order allows processes to move around in the syntax. It also allows actions to move outwards from a process and to commute in certain cases. The commuting rules follow the normal intuitions [Lea 2008].

The structural order (\geq) is defined using an order (\triangleright) on single-threaded action sequences; $\vec{\sigma} \triangleright \vec{\tau}$ is defined only if $|\text{thrd}(\vec{\sigma}) \cup \text{thrd}(\vec{\tau})| = 1$. Formally, \triangleright is defined to be the least precongurence on single-threaded action sequences that satisfies the following.

- (A-NONLOCK) If σ is a write or speculation, τ is write or speculation and $\text{loc}(\sigma) = \text{loc}(\tau)$ implies $\text{val}(\sigma) = \text{val}(\tau)$ then $\sigma\tau \triangleright \tau\sigma$.
- (A-BEGIN) If α is a begin then $\langle s?p.f=v \rangle \alpha \triangleright \alpha \langle s?p.f=v \rangle$.
- (A-ACQUIRE) If α is write and β is an acquire then $\alpha\beta \triangleright \beta\alpha$.
- (A-RELEASE) If α is write and β is a release then $\beta\alpha \triangleright \alpha\beta$.
- (A-ABSORPTION1) If α is a write then $\alpha \triangleright \alpha\alpha$.
- (A-ABSORPTION2) If α and β are writes to the same location then $\beta\alpha \triangleright \alpha$.
- (A-ABSORPTION3) If α, β and β' are writes to the same location then $\beta\beta'\alpha \triangleright \beta'\beta\alpha$.

Define the reduction relation $A \rightarrow B$ to be the least relation satisfying the rules and axioms in Equations 6.1. Define \rightarrow^* to be the reflexive and transitive closure of \rightarrow . In the figure, \mathcal{BV} is the set of base values. We rely on many other standard auxiliary definitions¹.

¹In classes and methods, the parameters are bound. In statements, bound variables are preceded by val . In processes, bound names are preceded by v . There are no binders in contexts. We identify syntax up to renaming of bound variables and names and write $M\{\! \{v/s\} \}$ for the capture avoiding substitution of v for x in M . We assume similar notation for substitution of names for names and for substitution over other syntax categories.

Suppose d is declared as $\text{class } d(\vec{x})\{\vec{\mathcal{F}}.\vec{\mathcal{M}}\}$. Define the partial function ftype so that $\text{ftype}(d(\vec{a}).f) = \text{final } aT$ if $\vec{\mathcal{F}}\{\! \{d/\vec{x}\} \}$ contains the declaration “ $\text{final } aT f;$ ”, and likewise for racing and lock-protected fields. Define the partial function mtype so that $\text{mtype}(d(\vec{a}).m) = b;\vec{a}\vec{T} \rightarrow aT$ if $\vec{\mathcal{M}}\{\! \{d/\vec{x}\} \}$ contain the declaration $aT m(b\text{this}; \vec{a}\vec{T} \vec{x})\{M\}$.

To define the dynamics, it suffices to describe classes in terms of three partial functions (finals , nonfinals and mbody). If $\text{finals}(d(\vec{a})) = \vec{f}$ then \vec{f} are final fields of class d , similarly for nonfinals . If $\text{mbody}(d(\vec{a}).m) = \lambda\vec{x}.M$ then d implements method m with parameters \vec{x} and body M . The abstraction $\lambda\vec{x}.M$ is written $\lambda.M$ when \vec{x} is the empty sequence.

$\frac{(S\text{-NU-FREE})}{A \equiv A \mid (vp)(p:d(\vec{y}) \mid \text{free } p)}$ $\frac{(S\text{-PAR})}{A \mid A' \equiv A' \mid A}$ $\frac{(S\text{-PREFIX-SPECULATION})}{\top \Rightarrow (\alpha A) \parallel \langle s?p.f=v \rangle \Rightarrow (\alpha A') \geq \alpha(\top \Rightarrow A) \parallel \langle s?p.f=v \rangle \Rightarrow A'}$ $\frac{(S\text{-NU-SPECULATION})}{(vp)(\top \Rightarrow A \parallel \langle s?p.f=v \rangle \Rightarrow A') \equiv \top \Rightarrow ((vp)A) \parallel \langle s?p.f=v \rangle \Rightarrow ((vp)A')}$ $\frac{(S\text{-PAR-SPECULATION})}{B \mid (\top \Rightarrow A \parallel \langle s?p.f=v \rangle \Rightarrow A') \equiv \top \Rightarrow (B \mid A) \parallel \langle s?p.f=v \rangle \Rightarrow (B \mid A')}$	$\frac{(S\text{-NU-PAR})}{(vp)(B \mid A) \equiv B \mid ((vp)A)}$ $\frac{(S\text{-NU-PREFIX})}{(vp)\alpha A \equiv \alpha(vp)A}$ $\frac{(S\text{-PREFIX-PAR})}{B \mid (\alpha A) \geq \alpha(B \mid A)}$ $\frac{(S\text{-PREFIX})}{\vec{\alpha} A \geq \vec{\beta} A}$ $\frac{(R\text{-STRUCTURAL-ORDER})}{A \geq B \quad B \rightarrow B' \quad B' \geq A' \quad A \rightarrow A'}$ $\frac{(R\text{-CAST})}{C \ni p:D \quad C \parallel [s[\text{val } x = (D)p; M]] \rightarrow C \parallel [s[M\{\! p/x\! \}]]}$ $\frac{(R\text{-OPERATOR})}{w \text{ is the result of } op \text{ on } \vec{v} \quad C \parallel [s[\text{val } x = op(\vec{v}); M]] \rightarrow C \parallel [s[M\{\! v/x\! \}]]}$ $\frac{(R\text{-IF-TRUE})}{C \parallel [s[\text{if } (\text{true}) \{M\} \text{ else } \{N\}]] \rightarrow C \parallel [s[M]]}$ $\frac{(R\text{-IF-FALSE})}{C \parallel [s[\text{if } (\text{false}) \{M\} \text{ else } \{N\}]] \rightarrow C \parallel [s[N]]}$	$\frac{(R\text{-METHOD-CALL})}{C \ni p:D \quad \text{mbody}(D.m) = \lambda \vec{v}.N \quad D \text{ not reserved} \quad m \neq \text{start}}$ $\frac{(R\text{-METHOD-RETURN})}{C \parallel [s[\text{val } x = \{\uparrow v\}M]] \rightarrow C \parallel [s[M\{\! v/x\! \}]]}$ $\frac{(R\text{-METHOD-CONTEXT})}{C \parallel [s[N]] \rightarrow C' \parallel [s[N']]} \quad C \parallel [s[\text{val } x = \{N\}M]] \rightarrow C' \parallel [s[\text{val } x = \{N'\}M]]$ $\frac{(R\text{-START})}{C \ni p:D \quad \text{mbody}(D.\text{run}) = \lambda \vec{v}.N \quad D \text{ not reserved} \quad C \ni \ell:\text{Lock}}$ $\frac{(R\text{-NEW})}{C \ni p:D \quad \text{finals}(D) = \vec{f} \quad \text{nonfinals}(D) = \vec{g} \quad D \text{ not reserved}}$ $\frac{(R\text{-FIELD-READ})}{C, s \text{ justifies read } p.f=v \quad C \parallel [s[\text{val } x = p.f; M]] \rightarrow C \parallel [s[M\{\! v/x\! \}]]}$ $\frac{(R\text{-FIELD-WRITE})}{C, s \text{ justifies end } p \quad C \parallel [s[\text{end } p; M]] \rightarrow C \parallel [s[M]]}$ $\frac{(R\text{-SPECULATION-OPEN})}{s \in \text{thrs}(A) \quad p \in \text{objs}(A) \quad v \in \mathcal{B}' \cup \text{objs}(A) \quad C, s \text{ justifies speculation } p.f=v}$ $\frac{(R\text{-SPECULATION-CLOSE})}{C \parallel [s[\text{val } x = p.f; M]] \rightarrow C \parallel [s[M\{\! v/x\! \}]] \quad C \parallel [\top \Rightarrow A \parallel \langle s?p.f=v \rangle \Rightarrow A] \rightarrow C \parallel [\top \Rightarrow A \parallel \langle s?p.f=v \rangle \Rightarrow B] \rightarrow C \parallel [B]}$
$\frac{(R\text{-ATOMIC-NEW})}{C \parallel [\text{free } \ell \mid \ell:\text{Atomic} \mid s[\text{val } x = \text{new Atomic}(v); M]] \rightarrow C \parallel [\ell:\text{Atomic}\{v; 1\} \mid \langle s!\ell \rangle \langle s!\ell:1 \rangle s[M\{\! v/x\! \}]]}$ $\frac{(R\text{-ATOMIC-GET})}{C \parallel [\ell:\text{Atomic}\{v; j\} \mid s[\text{val } x = \ell.\text{get}(); M]] \rightarrow C \parallel [\ell:\text{Atomic}\{v; j'\} \mid \langle s!\ell:j' \rangle s[M\{\! v/x\! \}]]}$ $\frac{(R\text{-ATOMIC-SET})}{C \parallel [\ell:\text{Atomic}\{v; j\} \mid s[\text{val } x = \ell.\text{set}(w); M]] \rightarrow C \parallel [\ell:\text{Atomic}\{w; j'\} \mid \langle s!\ell:j' \rangle s[M\{\! v/x\! \}]]}$	$\frac{(R\text{-ATOMIC-GET\&SET})}{C \parallel [\ell:\text{Atomic}\{v; j\} \mid s[\text{val } x = \ell.\text{gas}(w); M]] \rightarrow C \parallel [\ell:\text{Atomic}\{w; j'+1\} \mid \langle s!\ell:j' \rangle \langle s!\ell:j'+1 \rangle s[M\{\! v/x\! \}]]}$ $\frac{(R\text{-ATOMIC-COMPARE\&SET-FALSE})}{C \parallel [\ell:\text{Atomic}\{v; j\} \mid s[\text{val } x = \ell.\text{cas}(u, w); M]] \rightarrow C \parallel [\ell:\text{Atomic}\{v; j\} \mid s[M\{\! \text{null}/x\! \}]] \quad u \neq v$ $\frac{(R\text{-ATOMIC-COMPARE\&SET-TRUE})}{C \parallel [\ell:\text{Atomic}\{v; j\} \mid s[\text{val } x = \ell.\text{cas}(v, w); M]] \rightarrow C \parallel [\ell:\text{Atomic}\{w; j'+1\} \mid \langle s!\ell:j' \rangle \langle s!\ell:j'+1 \rangle s[M\{\! v/x\! \}]]}$	$j' = j+1 \text{ if } j \text{ odd}$ $j' = j \text{ if } j \text{ even}$ $C \text{ enables } \langle s!\ell:j \rangle$ $j' = j+1 \text{ if } j \text{ odd}$ $j' = j \text{ if } j \text{ even}$ $C \text{ enables } \langle s!\ell:j \rangle$

Figure 2. Structural order ($A \geq B$) and reduction ($A \rightarrow B$)

Many of the reduction rules are standard. We very briefly discuss some of the exceptions.

Atomic objects maintain a shared global state, enforcing sequential consistency. The state includes an integer, which is odd if the last operation was a set. These objects generate synchronization actions, which define the synchronizes-with relation. Even actions may be seen as an get/acquire and odd actions a set/release. In the formal development, we take it that cas returns null if the comparison fails, and the prior object otherwise.

As in Java, the reserved method start starts method run under the thread identity of the receiving object; this is a synchronization event enforced using a fresh “dummy” lock.

R-FIELD-WRITE describes field writes in a relaxed memory model, so the field writes become actions that float into the evaluation context. Field reads, R-FIELD-READ, may take any value that is justified by the evaluation context. R-END removes an end if the creation of the object reference is in a happens-before relationship; this is used in the following sections on typing.

Speculation can occur at any point, using R-SPECULATION-OPEN. The initial branch has guard \top , indicating that this branch

may make no additional assumptions. The final branch has a speculative action as its guard that can be used to justify reads. The context rules permit each branch of speculation evolve independently. Results from an active speculation can only leak to the outside world via S-PREFIX-SPECULATION if all branches produce the same action. This is significant, since only actions that manage to make it outside of a speculation may be used to finalize via R-SPECULATION-CLOSE.

6.2 Defining soundness

Soundness is defined in terms of stuck threads, which are defined in terms of evaluation contexts, defined as follows.

$$\begin{aligned} \mathbb{E} &::= [-] \mid \text{val } x = \{\mathbb{E}\} M && \text{(Statement ctxt)} \\ \mathbb{C} &::= [-] \mid \mathbb{C} \mid A \mid \mathbb{C} \mid (vp)\mathbb{C} \mid \alpha \mathbb{C} && \text{(Process ctxt)} \\ &\mid \top \Rightarrow \mathbb{C} \parallel \langle s?p.f=v \rangle \Rightarrow A \mid \top \Rightarrow A \parallel \langle s?p.f=v \rangle \Rightarrow \mathbb{C} \end{aligned}$$

Roughly, a stuck thread is a non-terminated thread that cannot reduce. As discussed by Igarashi, Pierce, and Wadler [2001], Java allows runtime type exceptions which are not considered a failure

of the static type system. Here we also have null pointer exceptions².

Definition 1 (Stuck thread). Thread s is *stuck* in A if there exists $\mathbb{C} \llbracket s \llbracket \mathbb{E} \llbracket M \rrbracket \rrbracket \rrbracket = A$ such that *none* of the following hold.

- (1) s has terminated: M is a return statement and $\mathbb{E} = \llbracket - \rrbracket$;
- (2) s can reduce: $\mathbb{C} \llbracket s \llbracket \mathbb{E} \llbracket M \rrbracket \rrbracket \rrbracket \rightarrow \mathbb{C}' \llbracket s \llbracket M' \rrbracket \rrbracket \rrbracket$ for some \mathbb{C}', M' ;
- (3) s has had a class cast exception: $M = \text{val } x = (D)v$; M' and \mathbb{C} does not contain subterm $v:D$; or
- (4) s has had a null pointer exception: M is a method, end, read, write, conditional or synchronization statement with target `null`. \square

The stuck processes of most interest come about because of the premise of the reduction rule for end statements. In order for `end p` to reduce, it must be the case the action $\langle t!p \rangle$ happens-before, for some t . This is a global property of the system, tracking data flow.

Theorem 2 (Soundness). *Suppose the class table is well typed and method `Main.main()` is defined. Suppose further that A is a speculation-free process that is reachable from the bootstrap process, that is*

$$(\text{vm}) \text{ m:Main} \mid \text{m[m.main()];} \rightarrow \dots \rightarrow A.$$

Then A does not contain a stuck thread. \square

We restrict attention to speculation-free processes, because threads within the final branch of a “bad” speculation may get stuck. Typing ensures that such speculations can never be finalized.

7. Typing processes

The proof of soundness follows standard lines: We provide typing rules for processes and prove subject reduction and progress. The details of the proof are too long to include here. Indeed, even the invariant maintained by running processes is too long to fully describe here. In this section, we describe some of the most interesting typing rules for processes, which embody intermediate states of computation. We first discuss the rules for “standard” processes, then for speculation.

In order to type processes, we introduce *effects* and extend environments as follows.

$$\begin{aligned} \zeta, \xi &::= \wr p \wr \\ E &::= \dots \mid E, p:D \mid E, s:\text{lock } \ell \mid E, s \text{ returns } aD \\ &\quad \mid E, \zeta \mid E, s:\zeta \mid E, \ell:\zeta \mid E, \text{onacq } \Phi \zeta \end{aligned}$$

The effect $\wr p \wr$ indicates that $\langle t!p \rangle$ has occurred, for some t . Effects are associated with threads ($s:\zeta$) and synchronization objects, or *locks* ($\ell:\zeta$). Naked effects (ζ), used in statement typing, are associated with the “local” thread. The environment $s:\text{lock } \ell$ indicates that s holds lock ℓ . The `onacq` environment is used to type bad speculations, as discussed in Section 7.1.

Define $\text{locks}(s, E) = \{\text{lock } V \mid s:\text{lock } V \in E\}$ and $\text{effects}(s, E) = \{\zeta \mid s:\zeta \in E\}$. Define $E \Vdash_s \Phi$ to mean $E, \text{effects}(s, E), \text{locks}(s, E) \Vdash \Phi$, and similarly for $E \vdash_s V : aT$ and $E \vdash_s M : aT$. Let Z range over sets of effects.

The new rules for values and the key rules for processes ($E \vdash A$) and actions ($E \vdash \sigma \triangleright E'$) are given in Figure 3. The proof of soundness also uses *well-formedness*, which is defined in the full version of this paper. Well-formedness is trivially true of the bootstrap process and is preserved by structural order and reduction. Well-formedness eliminates nonsense processes, such as $s[M] \mid s[N]$ and $\langle s!p.f=v \rangle \langle t!p.f=w \rangle A$, where f is a final field.

The action sequence records the entire history of the running process. The type rules can compute the happens-before relation

²When formalizing synchronization statements, one must add a case for threads that are waiting to acquire a lock.

$$\begin{array}{c} \text{(VAL-OBJECT-}\square\text{)} \quad \text{(VAL-OBJECT-}\boxtimes\text{)} \\ \frac{E \vdash \diamond \quad E \ni p:D}{E \vdash p : \square D} \quad \frac{E \vdash \diamond \quad E \ni p:D \quad E \Vdash \wr p \wr}{E \vdash p : \boxtimes D} \\ \text{(PROC-ACTION)} \quad \text{(PROC-THREAD)} \\ \frac{E \vdash \alpha \triangleright E' \quad E' \vdash A}{E \vdash \alpha A} \quad \frac{E \ni s \text{ returns } aT \quad E \vdash_s M : aT}{E \vdash s[M]} \\ \text{(ACT-BEGIN)} \\ \frac{s \in \text{dom}(E) \quad p \in \text{dom}(E)}{E \vdash \langle s!p \rangle \triangleright E, s:\wr p \wr} \\ \text{(ACT-ACQUIRE)} \\ \frac{s \in \text{dom}(E) \quad E \ni \ell:\text{Lock} \quad E' = \{s:\zeta \mid E \ni \text{onacq } \Phi \zeta \text{ and } E \Vdash_s \Phi\}}{E \vdash \langle s!\ell:j \rangle \triangleright E, E', s:\text{lock } \ell, s:\text{effects}(\ell, E)} \quad j \text{ even} \\ \text{(ACT-RELEASE)} \\ \frac{s \in \text{dom}(E) \quad E \ni \ell:\text{Lock}}{E, s:\text{lock } \ell \vdash \langle s!\ell:j \rangle \triangleright E, \ell:\text{effects}(s, E)} \quad j \text{ odd} \\ \text{(ACT-}\boxtimes\text{-GUARDED-WRITE)} \\ \frac{s \in \text{dom}(E) \quad E \ni p:D \quad \text{ftype}(D.f) = \boxtimes S \text{ wrguard } \Psi \quad E \vdash_s v : \boxtimes S \quad E \ni t:\wr p \wr \text{ implies } E \Vdash_s \Psi\{\wr p \wr\}}{E \vdash \langle s!p.f=v \rangle \triangleright E} \end{array}$$

Figure 3. Selected type rules for processes (actions and threads)

$$\begin{array}{c} \text{(ACT-}\boxtimes\text{-GUARDED-SPECULATION)} \\ \frac{s \in \text{dom}(E) \quad E \ni p:D \quad E \vdash_s v : \square S \quad \text{ftype}(D.f) = \boxtimes S \text{ rdguard } \Phi \quad E' = \{t:\wr v \wr \mid E \Vdash_t \Phi\{\wr p \wr\}\}}{E \vdash \langle s?p.f=v \rangle \triangleright E, E', \text{onacq } \Phi\{\wr p \wr\}\} (\wr v \wr) \\ \text{(ACT-}\boxtimes\text{-FINAL-SPECULATION)} \\ \frac{s \in \text{dom}(E) \quad E \vdash_s v : \square S \quad E \ni p:D \quad \text{ftype}(D.f) = \text{final } \boxtimes S}{E \vdash \langle s?p.f=v \rangle \triangleright E, \{t:\wr v \wr \mid E \Vdash_t \wr p \wr\}} \\ \text{(PROC-SPECULATION)} \\ \frac{E \vdash A \quad E \vdash \phi \triangleright E' \quad E' \vdash B}{E \vdash \top \Rightarrow A \parallel \phi \Rightarrow B} \\ \text{(PROC-SPECULATION-SHAPE-ERROR)} \\ \frac{s \in \text{dom}(E) \quad E \ni p:D \quad E \vdash A \quad \text{ftype}(D.f) = \dots D' \dots \text{ and } E \ni v:D'' \text{ and } D'' \neq D'}{E \vdash \top \Rightarrow A \parallel \langle s?p.f=v \rangle \Rightarrow B} \end{array}$$

Figure 4. Selected type rules for processes (speculation)

as it moves through the action context. The same is true for the locks held at any point. Subject reduction uses the fact that such sequences are stable up to single-threaded action reordering.

ACT-ACQUIRE adds a lock to the environment and copies effects from locks to threads. ACT-RELEASE removes a lock from the environment and copies effects from threads to locks. (We discuss `onacq` in in Section 7.1.)

Using PROC-ACTION and ACT-BEGIN, the effect $\wr p \wr$ is available to A in $\langle t!p \rangle A$. The VAL-OBJECT rules subsequently require that $\wr p \wr$ be present in order to type p at \boxtimes .

ACT- \boxtimes -GUARDED-WRITE directly parallels STAT-GUARDED-WRITE, ensuring that appropriate locks and effects are in scope. The locks are only necessary after the fields of p are initialized and thus $t:\wr p \wr$ is in scope, for some t . This rule is preserved by action reordering: Although write actions can commute with some write actions, the rules require that locks can only expand their scope.

7.1 Speculation

Several of the typing rules for speculation are given in Figure 4.

The speculation $\top \Rightarrow A \parallel \langle s?p.f=q \rangle \Rightarrow B$ allows the justifying branch A and final branch B to proceed independently, and also allows the final branch to speculate on a future write $\langle s!p.f=q \rangle$. The operational semantics does not constrain the speculated write, so the speculated write may not be well-typed. Consequently, a read of the speculated write by the final branch process B can cause the resulting final branch to fail to type. Moreover, the speculation can reduce to the final branch if the speculated write is justified by the speculation's context. Thus it is natural to ask whether a process with ill-typing confined to the final branch of a speculation can reduce to a process with ill-typing outside speculation.

There are two classes of typing errors that may arise. First, shape errors may arise when a speculated write refers to an object with an incompatible class. For example, the speculated write has a shape error in the following process, where $p:C7$, $q:Object$.

```
class C7 {C7 f;}
 $\top \Rightarrow \dots \parallel \langle s?p.f=q \rangle \Rightarrow \dots$ 
```

Such shape errors are permitted in final branches of speculations. However, such speculations are *unfinalizable* since a well-typed justifying branch, which does not see the speculation, cannot produce the necessary write to justify the speculation. The use of the justifying branch of a speculation as a filter is critical to prevent ill-typed actions leaking from the final branch of a speculation.

Type errors may also arise in the final branch of a speculation due to a misuse of effects, but such speculations are also unfinalizable. Unlike shape errors, it is not immediately evident that an effect error has occurred. Our approach with effects, therefore, is to saturate the final branch with sufficient effects to ensure that no type errors arise, while ensuring that all of the imparted effects can be justified when the speculation is finalized.

A read of the speculated write may convey information about happens-before relationships via \boxtimes -annotated types that is not justified by the process context. For example, a speculated write $\langle s?p.f=q \rangle$ may be read in the final branch of a speculation in the absence of a happens-before relationship with $\downarrow q$ (thus preventing a write action $\langle s!p.f=q \rangle$ being typeable at the point of the speculation).

The type system accommodates potential memory-effect errors by associating *speculated effects* $\downarrow q$ with speculated writes $\langle s?p.f=q \rangle$, whenever the corresponding write $\langle s!p.f=q \rangle$ would convey a memory effect. This allows the final branches of speculations to be typed even if reduction reads speculated writes that do not yet convey happens-before relationships. Speculated effects can be discharged when their associated speculations are finalized because the justifying writes for the speculation appear outside the speculation and thus justify the speculated memory effects.

Typing for speculation on a final field does not require locks or latent effects. Instead, as with reads of final fields, it is significant whether a thread has the effect $\downarrow p$ given a speculated write $\langle s?p.f=q \rangle$. ACT- \boxtimes -FINAL-SPECULATION generates an environment extension with an effect $\downarrow q$ for all threads that prove $\downarrow p$. If the speculation on a final field's write is finalized, the use of ACT- \boxtimes -FINAL-SPECULATION can be discharged immediately in the resulting final branch.

It is worth emphasizing that the additional effects provided by speculative fields are only necessary for speculations that cannot be finalized. Our semantics has been designed to allow as many speculations as possible.

Alternatively, one could attempt to disallow “useless” speculations, which either (1) cannot be finalized or (2) add nothing to the execution. For example, one can show that speculation with a value of the wrong shape is useless, for the first reason; speculating on a lock-protected field is useless, for the second reason. It seems difficult, however to prohibit useless speculation on final fields, which

communicate effects. In addition, our approach can be adapted to richer effect languages.

7.2 Typing examples

The environments used to type processes have some shared information, common to all threads, such as the type of an object reference p . In addition, environments record local information including effects held on a per thread basis. Consequently, each thread may have a different view of the effects associated with an object.

For example, consider the following process (we leave names free, and omit uninteresting local variable bindings and continuation terms). Let $p:C5$.

```
class C5 {final  $\boxtimes$ Lock l;  $\boxtimes$ Object f guard this.l;}
s[ $\text{val } x = \text{new Object}(); \text{sync } p.l\{p.f=x;\}$ ] |
t[ $\text{sync } p.l\{\text{val } y=p.f; \text{end } y;\}$ ]
```

After the constructor, thread s will be typed in an environment including $x:\boxtimes \text{Object}$. In the presence of suitable auxiliary processes such as “free q ”, the above process may reduce to the following, where $p:C5$, $q:Object$.

$$\langle s!q \rangle (s[\text{sync } p.l\{p.f=q;\}] \mid t[\text{sync } p.l\{\text{val } y=p.f; \text{end } y;\}])$$

The environment for typing the threads includes $p:C5$, $q:Object$, $s:\downarrow q$. The localized effect $s:\downarrow q$ represents the fact that code running at thread s has a happens-before relationship with q 's constructor. This effect is introduced into the environment by PROC-ACTION's use of ACT-BEGIN when typing the action $\langle s!p \rangle$. The same effect is required to justify the typing of q as $\boxtimes \text{Object}$ by VAL-OBJECT- \boxtimes . Without the effect $s:\downarrow q$, uses of q could only be typed as $\square \text{Object}$ via VAL-OBJECT- \square .

The effect $s:\downarrow q$ cannot be used directly within thread t , but is communicated in the statics (as in the dynamics) via memory synchronization actions such as lock acquisition and release. To see this, consider a further three reductions by thread s above. We assume that a write of k to $p.l$ by some thread is visible. (We apply structural reordering for readability and again omit auxiliary processes. We assume that lock k has not yet been used before). Let $p:C5$, $q:Object$, $k:Lock$.

$$\langle s!q \rangle \langle s!k:0 \rangle \langle s!p.f=q \rangle \langle s!k:42 \rangle (s[] \mid t[\text{sync } p.l\{\text{val } y=p.f; \text{end } y;\}])$$

The environment records that lock k is held by thread s between the actions $\langle s!k:0 \rangle$ and $\langle s!k:42 \rangle$. In particular, this justifies the write $\langle s!p.f=q \rangle$ to a \boxtimes field guarded by k . Moreover, the assignment of q to a \boxtimes field in $\langle s!p.f=q \rangle$ requires the $s:\downarrow q$ effect in the environment at the point of the write, mirroring the assignment typing in terms described above.

Further reductions lead to the following process where the lock k has been acquired by t , and q has been read from $p.f$ then substituted for z . Let $p:C5$, $q:Object$, $k:Lock$.

$$\langle s!q \rangle \langle s!k:0 \rangle \langle s!p.f=q \rangle \langle s!k:42 \rangle \langle t!k:27 \rangle (s[] \mid t[\{\text{end } q;\} k.\text{release}();])$$

Here the typing of q via STAT-END requires that q be typed as $\boxtimes \text{Object}$ within thread t , and, as before, this requires that the environment provides $\downarrow q$ for thread t . The lock release $\langle s!k:42 \rangle$ and lock acquire $\langle t!k:27 \rangle$ achieve this by transferring the effects known to thread s ($s:\downarrow q$) to lock k ($k:\downarrow q$) at the release of k by thread s , and transferring the effects for lock k to thread t ($t:\downarrow q$) at the acquisition of k by thread t . Thus we type the threads in an environment including $t:\downarrow q$. In this way, the type system calculates communication of effects between threads following the memory synchronization actions.

The typing of final fields necessarily differs from the typing of fields guarded by locks because there are no memory synchronization actions used when reading a final field. The key observation in reading a final field is that the object reference through which the final field is read must be \boxtimes annotated. For example, in typing the following process, $(\text{end } x)$ is only permitted if $x : \boxtimes \text{Object}$, and this in turns requires $s : [p]$ in the typing environment for the process. Let $p : C6$.

```
class C6 {final  $\boxtimes$ Object f;}
s[val x=p.f; end x;]
```

This is expressed in the rule STAT-FINAL-READ.

Although writes to final fields may migrate via the structural order, this invariant is sufficient because writes to final fields always happen-before the begin representing construction of the object. The \boxtimes nature of a value q read from a final field $p.f$ then follows from transitivity of the happens-before relation between: (1) the construction of q and the write to $p.f$; (2) the write to $p.f$ and the begin for p ; (3) the begin for p and its receipt at \boxtimes type; and (4) receipt of p and the read $p.f$. This property demonstrates how happens-before relationships can be established for lockless traversal of immutable portions of data structures whenever a happens-before relationship is established with the construction of the initial object reference.

8. Related work and conclusion

We have already cited several related pieces of work in context in the paper. We refer the reader to [Steinke and Nutt 2004] and [Adve and Gharachorloo 1996] for surveys of memory models for hardware architectures. Saraswat [2004] provides a framework for operational semantics with relaxed memory models for typed languages. Our prior work [Jagadeesan, Pitcher, and Riely 2010] uses the techniques of Cenciarelli, Knapp, and Sibilio [2007] and Boudol and Petri [2009] to accommodate the full expressiveness of the JMM.

The \boxtimes annotation uses objects to witness side-effects [Terauchi and Aiken 2008]. However, in contrast to the large research on data races, we only enforce the absence of read-write data races on our witness objects and allow write-write data races. Happens-before plays a key role in the static analysis of programs, e.g., vector-clock algorithms for race detection [Flanagan and Freund 2009] or the characterization of execution paths [Burckhardt, Kothari, Musuvathi, and Nagarakatte 2010].

There has also been recent work on static analysis for weak memory models [Ferrara 2008; Miné 2011; Alglave, Kroening, Lugton, Nimal, and Tautschnig 2011] and provably correct compilers targeting weak memory Sevcík, Vafeiadis, Nardelli, Jagannathan, and Sewell [2011].

The study of relaxed memory models has hitherto focused on the dynamic semantics of programs. This paper has focused on static semantics. We have argued that correspondence assertions are the correct formalism for reasoning about happens-before relations, and that these correspondences can be specified and validated by enriching interfaces with notation for memory effects. Our techniques apply to common data structures from `java.util.concurrent`, including both lock-based and non-blocking implementations.

Our analysis intentionally includes only a small fragment of the type system of Abadi, Flanagan, and Freund [2006] for data race detection. Fully integrating other techniques used there (such as existential types in the form of ghost variables) and other existing techniques, including analyses based on locality, confinement, linearity, monotonicity, and effects is a topic of future research.

References

- M. Abadi, C. Flanagan, and S. N. Freund. Types for safe locking: Static race detection for Java. *TOPLAS*, 28(2):207–255, 2006.
- S. V. Adve and K. Gharachorloo. Shared memory consistency models: A tutorial. *Computer*, 29(12):66–76, 1996.
- J. Alglave, D. Kroening, J. Lugton, V. Nimal, and M. Tautschnig. Soundness of data flow analyses for weak memory models. In *APLAS*, 2011.
- H.-J. Boehm and S. V. Adve. Foundations of the C++ concurrency memory model. In *PLDI '08*, pages 68–78, 2008.
- G. Boudol and G. Petri. Relaxed memory models: an operational approach. In *POPL*, pages 392–403, 2009.
- S. Burckhardt, P. Kothari, M. Musuvathi, and S. Nagarakatte. A randomized scheduler with probabilistic guarantees of finding bugs. In *ASPLOS*, pages 167–178, 2010.
- P. Cenciarelli, A. Knapp, and E. Sibilio. The Java memory model: Operationally, denotationally, axiomatically. In *ESOP*, pages 331–346, 2007.
- P. Ferrara. Static analysis via abstract interpretation of the happens-before memory model. In *TAP*, pages 116–133, 2008.
- C. Flanagan and S. N. Freund. Fasttrack: efficient and precise dynamic race detection. In *PLDI*, pages 121–133, 2009.
- B. Goetz, T. Peierls, J. Bloch, J. Bowbeer, D. Holmes, and D. Lea. *Java Concurrency in Practice*. Addison-Wesley Professional, 2005.
- A. D. Gordon and A. Jeffrey. Typing correspondence assertions for communication protocols. *Theor. Comput. Sci.*, 300:379–409, 2003.
- C. Hawblitzel. Linear types for aliased resources. Technical Report MSR-TR-2005-141, Microsoft Research, 2005.
- A. Igarashi, B. C. Pierce, and P. Wadler. Featherweight Java: a minimal core calculus for Java and GJ. *ACM TOPLAS*, 23(3):396–450, 2001.
- R. Jagadeesan, C. Pitcher, and J. Riely. Generative operational semantics for relaxed memory models. In *ESOP*, 2010.
- L. Lamport. How to make a multiprocessor computer that correctly executes multiprocess program. *IEEE Trans. Comput.*, 28(9):690–691, 1979.
- D. Lea. The JSR-133 cookbook for compiler writers. <http://gee.cs.oswego.edu/dl/jmm/cookbook.html>, 2008.
- J. M. Lucassen and D. K. Gifford. Polymorphic effect systems. In *POPL*, pages 47–57, 1988.
- J. Manson, W. Pugh, and S. V. Adve. The Java memory model. In *POPL '05*, pages 378–391, 2005.
- A. Miné. Static analysis of run-time errors in embedded critical parallel c programs. In *ESOP*, pages 398–418, 2011.
- V. A. Saraswat. Concurrent constraint-based memory machines: A framework for Java memory models. In *ASIAN*, 2004.
- J. Sevcík. *Program Transformations in Weak Memory Models*. PhD, Univ. of Edinburgh, 2008.
- J. Sevcík and D. Aspinall. On validity of program transformations in the Java memory model. In *ECOOP*, pages 27–51, 2008.
- J. Sevcík, V. Vafeiadis, F. Z. Nardelli, S. Jagannathan, and P. Sewell. Relaxed-memory concurrency and verified compilation. In *POPL*, 2011.
- P. Sewell. Global/local subtyping and capability inference for a distributed π -calculus. In *ICALP '98, LNCS 1443*, pages 695–706, 1998.
- R. C. Steinke and G. J. Nutt. A unified theory of shared memory consistency. *J. ACM*, 51(5):800–849, 2004.
- T. Terauchi and A. Aiken. Witnessing side effects. *TOPLAS*, 30(3), 2008.
- T. Y. C. Woo and S. S. Lam. Authentication for distributed systems. *IEEE Computer*, 25:39–52, January 1992.
- T. Zhao, J. Palsberg, and J. Vitek. Type-based confinement. *J. Funct. Program.*, 16:83–128, January 2006.

A. Syntax

Figure 5 collects the syntactic definitions from the text. The syntax differs from that of the body text only in that we have introduced speculative actions and used these to define speculative processes. We have also introduced a category for actions. For bookkeeping purposes, we also annotate partially executed method bodies and synchronization blocks with the identity of the thread in which they occur.

Figure 6 includes the additional reduction rules for locks and conditions.

B. Well-formed processes

Define *procs*, mapping contexts to processes, as follows.

$$\begin{aligned} \text{procs}(\llbracket - \rrbracket) &= \emptyset \\ \text{procs}(\alpha C) &= \text{procs}(C) & \text{procs}(\nu q) C &= \text{procs}(C) \\ \text{procs}(\top \Rightarrow A \parallel \phi \Rightarrow C) &= \text{procs}(C) & \text{procs}(A | C) &= A \uplus \text{procs}(C) \\ \text{procs}(\top \Rightarrow C \parallel \phi \Rightarrow A) &= \text{procs}(C) & \text{procs}(C | A) &= \text{procs}(C) \uplus A \end{aligned}$$

A process A is *well-formed* if it satisfies the following.

- (1) If A is $C \llbracket \text{free } s \rrbracket$, $C \llbracket \text{runnable } s \rrbracket$ or $C \llbracket s[M] \rrbracket$ then the following hold.
 - (a) C is contiguous.
 - (b) There is no $\text{free } s$, $\text{runnable } s$ or $s[M]$ in $\text{procs}(C)$.
 - (c) At most one of $\text{free } t$, $\text{runnable } t$ or $t[N]$ occurs in $\text{procs}(C)$, for any t .
 - (d) At most one $p:D$ occurs in $\text{procs}(C)$, for any p .
 - (e) If there is an action in $\text{act}(C, s)$ with source $t \neq s$, then $t[N]$ occurs in $\text{procs}(C)$.
 - (f) If p occurs in C or M , then $p:D$ occurs in $\text{procs}(C)$.
 - (g) If $p:D$ occurs in $\text{procs}(C)$ and $f \in \text{finals}(D)$, then there exists exactly one write action to $p.f$ in $\text{act}(C, s)$.
- (2) Let thrds and objs return the names of the potential threads and the objects of a process, respectively. For example $\text{thrds}(\text{free } s) = \text{thrds}(\text{runnable } s) = \text{thrds}(s[M]) = \{s\}$ and $\text{objs}(p:D) = \text{objs}(p:\text{Atomic}\{v; \mathbb{C}\}) = \text{objs}(p:\text{Lock}\{\bar{s}; j\}) = \{p\}$.

- (a) In any subprocess $B | B'$, we have that $\text{objs}(B) \cap \text{objs}(B') = \emptyset$, $\text{thrds}(B) \cap \text{thrds}(B') = \emptyset$.
- (b) In any subprocess αB , we have that $\text{thrd}(\alpha) \in \text{thrds}(B)$.
- (c) In any subprocess $\top \Rightarrow B \parallel \phi \Rightarrow B'$, we have that $\text{thrd}(\phi) \in \text{thrds}(B) \cap \text{thrds}(B')$.

Well-formedness is trivially true of the bootstrap process and is preserved by structural order and reduction.

Lemma 3. *The bootstrap process is well-formed.*

PROOF. Trivial. \square

Lemma 4. (a) *Suppose A is well-formed and $A \geq A'$ then A' is well-formed.*
(b) *Suppose A is well-formed and $A \rightarrow A'$ then A' is well-formed.*

PROOF. (a) follows by induction on the proof that $A \geq A'$. (b) follows by induction on the proof that $A \rightarrow A'$. \square

C. Typing details

The reserved methods `start` and `run` have type “ $\boxtimes; \emptyset \rightarrow \square \text{Unit}$.”

The reserved classes have the following signatures. Recall that we elide “ $\square \text{this}$,” from method interfaces. In the formal development, we take it that `cas` returns `null` if the comparison fails, and the prior object otherwise.

```
class Object { }
class Atomic(χ) {
  χ Object get();
  void set(χ Object v);
  χ Object gas(χ Object v);
  χ Object cas(Object oldv, χ Object newv);
}
class Lock { }
class Cond(χ) {
```

```
  χ Object await();
  void signal(χ Object);
}
```

We now present the judgments, divided into five groups: Judgements for satisfaction.

$$\begin{aligned} E \Vdash \Phi & \quad (\text{Guard satisfaction}) \\ E \Vdash \zeta & \quad (\text{Effect satisfaction}) \end{aligned}$$

Judgements for statics.

$$\begin{aligned} \vdash \mathcal{D} & \quad (\text{Ok class declaration}) \\ E \vdash \mathcal{M} \text{ in } D & \quad (\text{Ok method declaration}) \\ E \vdash \mathcal{F} & \quad (\text{Ok field declaration}) \\ E \vdash V : aT & \quad (\text{Ok value}) \\ E \vdash M : aT \triangleright Z & \quad (\text{Ok statement}) \end{aligned}$$

Judgements for dynamics.

$$\begin{aligned} E \vdash A & \quad (\text{Ok process}) \\ E \vdash \sigma \triangleright E' & \quad (\text{Ok action}) \end{aligned}$$

Supporting judgements.

$$\begin{aligned} E \vdash \diamond & \quad (\text{Ok environment}) \\ E \vdash T & \quad (\text{Ok type}) \\ E \vdash a & \quad (\text{Ok annotation}) \\ E \vdash F : T & \quad (\text{Ok path}) \\ E \vdash \Phi & \quad (\text{Ok guard}) \\ E \vdash \zeta & \quad (\text{Ok effect}) \end{aligned}$$

Judgements for contexts.

$$E \vdash C \triangleright E' \quad (\text{Ok context})$$

We identify environments up to reordering.

C.1 Judgements for satisfaction

Judgment $E \Vdash \Phi$.

$$\begin{aligned} \frac{E \ni \text{lock } F}{E \Vdash F} \quad \frac{E \Vdash \Phi \quad E \Vdash \Psi}{E \Vdash \Phi \&\&\Psi} \quad \frac{E \Vdash \Phi \quad E \Vdash \Psi}{E \Vdash \Phi \parallel \Psi} \quad \frac{E, E' \llbracket \bar{f}/x \rrbracket \Vdash \Phi}{E, x : aT, \text{final } x = F, E' \Vdash \Phi} \end{aligned}$$

Judgment $E \Vdash \zeta$.

$$\frac{E \ni \zeta \quad E, E' \llbracket \bar{f}/x \rrbracket \Vdash \zeta}{E \Vdash \zeta} \quad \frac{E, x : aT, \text{final } x = F, E' \Vdash \zeta}{E \Vdash \zeta}$$

C.2 Judgements for statics

Let \mathbb{F} and \mathbb{G} range over sets of field paths. Then any guard Φ can be written in disjunctive normal as $\bigvee_i \mathbb{F}_i$, where each set \mathbb{F}_i is viewed as a conjunction of field paths. We say that Φ *must overlap* with Ψ if $\Phi = \bigvee_i \mathbb{F}_i$, $\Psi = \bigvee_j \mathbb{G}_j$, and $\mathbb{F}_i \cap \mathbb{G}_j \neq \emptyset$ for all i, j .

Judgments $\vdash \mathcal{D}$ and $E \vdash \mathcal{M} \text{ in } D$.

$$\frac{\forall i. \bar{x} \vdash \mathcal{F}_i \quad \forall j. \bar{x} \vdash \mathcal{M}_j \text{ in } d(\bar{x})}{\vdash \text{class } d(\bar{x}) \{ \mathcal{F}, \mathcal{M} \}} \quad \frac{E, \text{this} : bD, \bar{x} : \bar{a}\bar{T} \vdash M : aT}{E \vdash aT m(b\text{this}; \bar{a}\bar{T} \bar{x}) \{M\} \text{ in } D}$$

Judgment $E \vdash \mathcal{F}$.

$$\frac{E \vdash T \quad E \vdash a \quad E \vdash D}{E \vdash T f; \quad E \vdash \text{final } aDf; \quad E \vdash aDf \text{ rdguard } \Phi \text{ wrguard } \Psi;}$$

Judgment $E \vdash V : aT$.

$$\frac{(\text{VAL-SUB-TYPE}) \quad E \vdash V : aD}{E \vdash V : a\text{Object}} \quad \frac{(\text{VAL-SUB-ANN1}) \quad E \vdash V : aT}{E \vdash V : \square T} \quad \frac{(\text{VAL-SUB-ANN2}) \quad E \vdash V : \boxtimes T}{E \vdash V : aT}$$

(VAL-BASE)

$$\frac{E \vdash \diamond \quad bv \text{ inhabits } bt}{E \vdash bv : \square bt}$$

$bt \in \mathcal{BT} \supseteq \{\text{Unit}, \text{Bool}, \text{Int}\}$	(Base type)
$f, g \in \mathcal{FN}$	(Field name)
$m \in \mathcal{MN} \supseteq \{\text{start}, \text{run}\}$	(Method name)
$d \in \mathcal{CN} \supseteq \{\text{Object}, \text{Atomic}, \text{Lock}, \text{Cond}\}$	(Class name)
$\chi \in \mathcal{AV}$	(Annotation variable)
$\gamma ::= \boxtimes \mid \square$	(Ground annotation)
$a, b, c ::= \chi \mid \gamma$	(Annotation)
$D ::= d(\vec{a})$	(Class type)
$T, S ::= D \mid bt$	(Type)
$\mathcal{D} ::= \text{class } d(\vec{\chi})\{\vec{\mathcal{F}}, \vec{\mathcal{M}}\}$	(Class declaration)
$\mathcal{M} ::= aT \ m(b\text{this}; \vec{a}\vec{T} \vec{x})\{M\}$	(Method declaration)
$\mathcal{F} ::= Tf; \mid \text{final } aDf; \mid aDf \text{ rdguard } \Phi \text{ wrguard } \Psi;$	(Field declaration)
$\Phi, \Psi ::= \text{true} \mid F \mid \Phi \&\& \Psi \mid \Phi \parallel \Psi$	(Guard)
$F, G ::= V \mid F.f$	(Field path)
$bv \in \mathcal{BV} \supseteq \{\text{unit}, \text{true}, \text{false}, 0, 42\}$	(Base value)
$op \in \mathcal{OP} \supseteq \{=, +\}$	(Base value operators)
$x, y \in \mathcal{VN} \supseteq \{\text{this}\}$	(Variable name)
$p, q, s, t, \ell \in \mathcal{ON}$	(Object name)
$v, w, u ::= \text{null} \mid bv \mid p$	(Ground value)
$V, W, U ::= v \mid x$	(Open value)
$M, N ::= \text{return } V; \mid \text{val } x = \text{new } D(\vec{V}); M \mid \text{val } x = W.m(\vec{V}); M \mid \text{val } x = op(\vec{V}); M \mid \text{val } x = (D)V; M \mid \text{end } V; M \mid \text{val } x = V.f; M \mid V.f = W; M \mid \text{if } (V) \{M\} \text{else } \{N\} \mid \text{val } x = \text{sync } V \{N\} M \mid \text{val } x = {}_s\{N\} M \mid \text{val } x = {}_s\{N\} \ell.\text{release}(); M$	(Statement)
$\mathbb{E} ::= \llbracket - \rrbracket \mid \text{val } x = {}_s\{\mathbb{E}\} M \mid \text{val } x = {}_s\{\mathbb{E}\} p.\text{release}(); M$	(Statement ctxt)
$\alpha, \beta ::= \langle s!p.f=v \rangle \mid \langle s!\ell:j \rangle \mid \langle s!p \rangle$	(Empirical Action)
$\phi ::= \langle s?p.f=v \rangle$	(Speculative action)
$\sigma, \tau ::= \alpha \mid \phi \mid s\llbracket - \rrbracket$	(Action)
$A, B ::= p:D \mid \ell:\text{Atomic}\{v; j\} \mid \ell:\text{Lock}\{\vec{s}; j\} \mid p:\text{Cond}\{\ell; \vec{s}; \vec{t}\} \mid \alpha A \mid s[M] \mid A \mid B \mid (vp)A \mid \text{free } p \mid \text{runnable } p \mid \top \Rightarrow A \mid \phi \Rightarrow B$	(Process)
$\mathbb{C} ::= \llbracket - \rrbracket \mid \mathbb{C} \mid A \mid \mathbb{C} \mid (vp)\mathbb{C} \mid \alpha \mathbb{C} \mid \top \Rightarrow \mathbb{C} \mid \phi \Rightarrow A \mid \top \Rightarrow A \mid \phi \Rightarrow \mathbb{C}$	(Process ctxt)
$\zeta, \xi ::= \wr p \}$	(Effect)
$Z ::= \{\zeta_1, \dots, \zeta_n\}$	(Set of effects)
$E ::= \emptyset \mid E, \chi \mid E, x:aT \mid E, p:D \mid E, s \text{ returns } aD \mid E, \text{final } x=F \mid E, \text{lock } V \mid E, s:\text{lock } \ell \mid E, \zeta \mid E, s:\zeta \mid E, \ell:\zeta \mid E, \text{onacq } \Phi \zeta$	(Environment)

Figure 5. Syntactic categories

(VAL-VARIABLE) $\frac{E \vdash \diamond E \ni x:aT}{E \vdash x:aT}$	(VAL-NUL) $\frac{E \vdash \diamond E \vdash a \quad E \vdash D}{E \vdash \text{null}:aD}$	(STAT-OPERATOR) $\frac{\text{op has type } \vec{bt} \rightarrow bt \quad E \vdash \vec{V} : \square \vec{bt}}{E, x:\square bt \vdash M : aT}$
(VAL-OBJECT- \square) $\frac{E \vdash \diamond E \ni p:D}{E \vdash p:\square D}$	(VAL-OBJECT- \boxtimes) $\frac{E \vdash \diamond E \ni p:D \quad E \Vdash \wr p\}$	$\frac{E \vdash \text{return } V; : aT}{E \vdash \text{val } x = op(\vec{V}); M : aT}$
Judgment $E \vdash M : aT$.		(STAT-CAST) $\frac{E \vdash V : b\text{Object} \quad E, x:bD \vdash M : aT}{E \vdash \text{val } x = (D)V; M : aT}$
(STAT-NEW) $\frac{\text{finals}(D) = \vec{f} \quad \text{ftype}(D.\vec{f}) = \text{final } \vec{b}\vec{S} \quad E \vdash \vec{V} : \vec{b}\vec{S} \quad E, x:\boxtimes D \vdash M : aT}{E \vdash \text{val } x = \text{new } D(\vec{V}); M : aT}$	(STAT-RACING-READ) $\frac{E \vdash V : \square D \quad \text{ftype}(D.f) = S \quad E, x:\square S \vdash M : aT}{E \vdash \text{val } x = V.f; M : aT}$	(STAT-RACING-WRITE) $\frac{E \vdash V : \square D \quad \text{ftype}(D.f) = S \quad E \vdash W : \square S \quad E \vdash M : aT}{E \vdash V.f = W; M : aT}$
(STAT-METHOD) $\frac{E \vdash W : cD \quad \text{mtype}(D.m) = c; \vec{b}\vec{S} \rightarrow bS \quad E \vdash \vec{V} : \vec{b}\vec{S} \quad E, x:bS \vdash M : aT}{E \vdash \text{val } x = W.m(\vec{V}); M : aT}$	(STAT-FINAL-READ) $\frac{E \vdash V : cD \quad \text{ftype}(D.f) = \text{final } bS \quad E, x:b'S, \text{final } x = V.f \vdash M : aT}{E \vdash \text{val } x = V.f; M : aT} \quad b' = \begin{cases} b, & \text{if } b = c \\ \square, & \text{otherwise} \end{cases}$	

<p>(R-LOCK-NEW)</p> $\frac{\mathbb{C} \llbracket \text{free } \ell \mid \ell : \text{Lock} \mid s[\text{val } x = \text{new Lock}(); M] \rrbracket}{\rightarrow \mathbb{C} \llbracket \ell : \text{Lock}\{\emptyset; 2\} \mid \langle s! \ell \rangle \langle s! \ell : 1 \rangle s[M\{\ell/x\}] \rrbracket}$ <p>(R-LOCK-ACQUIRE)</p> $\frac{\mathbb{C} \llbracket \ell : \text{Lock}\{\emptyset; j\} \mid s[\text{val } x = \text{sync } \ell \{N\} M] \rrbracket}{\rightarrow \mathbb{C} \llbracket \ell : \text{Lock}\{s; j\} \mid \langle s! \ell : j \rangle s[\text{val } x = s\{N\} \ell . \text{release}(); M] \rrbracket} \quad \mathbb{C} \text{ enables}(s! \ell : j)$ <p>(R-LOCK-RELEASE)</p> $\frac{\mathbb{C} \llbracket \ell : \text{Lock}\{s; j\} \mid s[\text{val } x = s\{\uparrow v\} \ell . \text{release}(); M] \rrbracket}{\rightarrow \mathbb{C} \llbracket \ell : \text{Lock}\{\emptyset; j+2\} \mid \langle s! \ell : j+1 \rangle s[M\{\uparrow v/x\}] \rrbracket}$ <p>(R-LOCK-ACQUIRE-REENTRANT)</p> $\frac{\mathbb{C} \llbracket \ell : \text{Lock}\{\bar{t}; j\} \mid s[\text{val } x = \text{sync } \ell \{N\} M] \rrbracket}{\rightarrow \mathbb{C} \llbracket \ell : \text{Lock}\{\bar{t} \uplus s; j\} \mid s[\text{val } x = s\{N\} \ell . \text{release}(); M] \rrbracket} \quad s \in \bar{t}$ <p>(R-LOCK-RELEASE-REENTRANT)</p> $\frac{\mathbb{C} \llbracket \ell : \text{Lock}\{\bar{t} \uplus s; j\} \mid s[\text{val } x = s\{\uparrow v\} \ell . \text{release}(); M] \rrbracket}{\rightarrow \mathbb{C} \llbracket \ell : \text{Lock}\{\bar{t}; j\} \mid s[M\{\uparrow v/x\}] \rrbracket}$ <p>(R-CONDITION-NEW)</p> $\frac{\mathbb{C} \llbracket \text{free } p \mid p : \text{Cond} \mid s[\text{val } x = \text{new Cond}(\ell); M] \rrbracket}{\rightarrow \mathbb{C} \llbracket p : \text{Cond}\{\ell; \emptyset; \emptyset\} \mid \langle s! p \rangle s[M\{p/x\}] \rrbracket} \quad \mathbb{C} \ni \ell : \text{Lock}\{\dots\}$ <p>(R-CONDITION-RELEASE)</p> $\frac{\mathbb{C} \llbracket p : \text{Cond}\{\ell; \bar{q}; Q\} \mid \ell : \text{Lock}\{\bar{t}; j\} \mid s[\text{val } x = p . \text{await}(); M] \rrbracket}{\rightarrow \mathbb{C} \llbracket p : \text{Cond}\{\ell; \bar{q} \uplus \bar{t}; Q\} \mid \ell : \text{Lock}\{\emptyset; j+2\} \mid \langle \bar{t}! \ell : j+1 \rangle s[\text{val } x = p . \text{await}(); M] \rrbracket} \quad s \in \bar{t}$ <p>(R-CONDITION-SIGNAL)</p> $\frac{\mathbb{C} \llbracket p : \text{Cond}\{\ell; \bar{q} \uplus \bar{s}; Q \mid s[\text{val } x = p . \text{signal}(v); M] \rrbracket}{\rightarrow \mathbb{C} \llbracket p : \text{Cond}\{\ell; \bar{q}; Q \uplus (\bar{s}, v)\} \mid s[M\{\text{unit}/x\}] \rrbracket} \quad \mathbb{C} \ni \ell : \text{Lock}\{\bar{t}; j\} \quad s \in \bar{t}$ <p>(R-CONDITION-ACQUIRE)</p> $\frac{\mathbb{C} \llbracket p : \text{Cond}\{\ell; \bar{q}; Q \uplus (\bar{t}, v)\} \mid \ell : \text{Lock}\{\emptyset; j\} \mid s[\text{val } x = p . \text{await}(); M] \rrbracket}{\rightarrow \mathbb{C} \llbracket p : \text{Cond}\{\ell; \bar{q}; Q \mid \ell : \text{Lock}\{\bar{t}; j\} \mid \langle s! \ell : j \rangle s[M\{\uparrow v/x\}] \rrbracket} \quad \mathbb{C} \text{ enables}(s! \ell : j) \quad s \in \bar{t}$	<p>(R-LOCK-CONTEXT)</p> $\frac{\mathbb{C} \llbracket s[N] \rrbracket \rightarrow \mathbb{C}' \llbracket s[N'] \rrbracket}{\mathbb{C} \llbracket s[\text{val } x = s\{N\} \ell . \text{release}(); M] \rrbracket \rightarrow \mathbb{C}' \llbracket s[\text{val } x = s\{N'\} \ell . \text{release}(); M] \rrbracket}$
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Figure 6. Reduction rules for locks and conditions ($A \rightarrow B$)

<p>(STAT-GUARDED-READ)</p> $\frac{E \vdash V : \square D \quad \text{ftype}(D.f) = bS \text{rdguard } \Phi \quad E \Vdash \Phi\{\uparrow/\text{this}\} \quad E, x : bS \vdash M : aT}{E \vdash \text{val } x = V.f; M : aT}$ <p>(STAT-GUARDED-WRITE)</p> $\frac{E \vdash V : \square D \quad \text{ftype}(D.f) = bS \text{wrguard } \Psi \quad E \Vdash \Psi\{\uparrow/\text{this}\} \quad E \vdash W : bS \quad E \vdash M : aT}{E \vdash V.f = W; M : aT}$ <p>(STAT-IF)</p> $\frac{E \vdash V : \square \text{boolean} \quad E \vdash M : aT \quad E \vdash N : aT}{E \vdash \text{if } (V) \{M\} \text{ else } \{N\} : aT}$ <p>(STAT-LOCK)</p> $\frac{E \vdash V : \square \text{Lock} \quad E, \text{lock } V \vdash N : bS \quad E, x : bS \vdash M : aT}{E \vdash \text{val } x = \text{sync } V \{N\} M : aT}$ <p>(STAT-LOCK-CONTEXT)</p> $\frac{E, s \text{ returns } bS \vdash N : bS \quad E \vdash p : \square \text{Lock} \quad E, s \text{ returns } aT, x : bS \vdash M : aT}{E, s \text{ returns } aT \vdash \text{val } x = s\{N\} p . \text{release}(); M : aT}$ <p>(STAT-METHOD-CONTEXT)</p> $\frac{E, s \text{ returns } bS \vdash N : bS \quad E, s \text{ returns } aT, x : bS \vdash M : aT}{E, s \text{ returns } aT \vdash \text{val } x = s\{N\} M : aT}$	<h3>C.3 Judgements for dynamics</h3> <p>Define $\text{locks}(s, E) = \{\text{lock } V \mid s : \text{lock } V \in E\}$ and $\text{effects}(s, E) = \{\zeta \mid s : \zeta \in E\}$. Define $E \Vdash_s \Phi$ to mean $E, \text{effects}(s, E), \text{locks}(s, E) \Vdash \Phi$, and similarly for $E \vdash_s V : aT$ and $E \vdash_s M : aT$.</p> <p>Judgment $E \vdash A$.</p> <table style="width:100%; border-collapse: collapse;"> <tr> <td style="width:33%; vertical-align: top;"> <p>(PROC-FREE)</p> $\frac{E \vdash \diamond \quad E \ni p : D}{E \vdash \text{free } p}$ <p>(PROC-ATOMIC)</p> $\frac{E \vdash \diamond \quad E \ni \ell : \text{Atomic} \quad v \in \mathcal{B}' \cup \text{dom}(E)}{E \vdash \ell : \text{Atomic}\{v; j\}}$ <p>(PROC-CONDITION)</p> $\frac{E \vdash \diamond \quad E \ni p : \text{Cond} \quad E \ni \ell : \text{Lock} \quad \bar{s} \cup \bar{t} \subseteq \text{dom}(E)}{E \vdash p : \text{Cond}\{\ell; \bar{s}; \bar{t}\}}$ <p>(PROC-PAR)</p> $\frac{E \vdash A \quad E \vdash B}{E \vdash A \mid B}$ <p>(PROC-ACTION)</p> $\frac{E \vdash \alpha \triangleright E' \quad E' \vdash A}{E \vdash \alpha A}$ <p>(PROC-SPECULATION-SHAPE-ERROR)</p> $\frac{s \in \text{dom}(E) \quad E \ni p : D \quad E \vdash A \quad \text{Either } \text{ftype}(D.f) = \dots bt \dots \text{ and } \neg(v \text{ inhabits } bt) \text{ or } \text{ftype}(D.f) = \dots D' \dots \text{ and } E \ni v : D' \text{ and } D' \neq D}{E \vdash \top \Rightarrow A \parallel \langle s? p.f=v \rangle \Rightarrow B}$ </td> <td style="width:33%; vertical-align: top;"> <p>(PROC-ALLOCATED)</p> $\frac{E \vdash \diamond \quad E \ni p : D}{E \vdash p : D}$ <p>(PROC-LOCK)</p> $\frac{E \vdash \diamond \quad E \ni \ell : \text{Lock} \quad s \in \text{dom}(E)}{E \vdash \ell : \text{Lock}\{j; s\}}$ <p>(PROC-THREAD)</p> $\frac{E \ni s \text{ returns } aT \quad E \vdash_s M : aT}{E \vdash s[M]}$ <p>(PROC-NU)</p> $\frac{E, p : D, p \text{ returns } \square \text{Unit} \vdash A}{E \vdash (vp)A}$ <p>(PROC-SPECULATION)</p> $\frac{E \vdash A \quad E \vdash \phi \triangleright E' \quad E' \vdash B}{E \vdash \top \Rightarrow A \parallel \phi \Rightarrow B}$ </td> <td style="width:33%; vertical-align: top;"> <p>(PROC-RUNNABLE)</p> $\frac{E \vdash \diamond \quad p \in \text{dom}(E)}{E \vdash \text{runnable } p}$ </td> </tr> </table>	<p>(PROC-FREE)</p> $\frac{E \vdash \diamond \quad E \ni p : D}{E \vdash \text{free } p}$ <p>(PROC-ATOMIC)</p> $\frac{E \vdash \diamond \quad E \ni \ell : \text{Atomic} \quad v \in \mathcal{B}' \cup \text{dom}(E)}{E \vdash \ell : \text{Atomic}\{v; j\}}$ <p>(PROC-CONDITION)</p> $\frac{E \vdash \diamond \quad E \ni p : \text{Cond} \quad E \ni \ell : \text{Lock} \quad \bar{s} \cup \bar{t} \subseteq \text{dom}(E)}{E \vdash p : \text{Cond}\{\ell; \bar{s}; \bar{t}\}}$ <p>(PROC-PAR)</p> $\frac{E \vdash A \quad E \vdash B}{E \vdash A \mid B}$ <p>(PROC-ACTION)</p> $\frac{E \vdash \alpha \triangleright E' \quad E' \vdash A}{E \vdash \alpha A}$ <p>(PROC-SPECULATION-SHAPE-ERROR)</p> $\frac{s \in \text{dom}(E) \quad E \ni p : D \quad E \vdash A \quad \text{Either } \text{ftype}(D.f) = \dots bt \dots \text{ and } \neg(v \text{ inhabits } bt) \text{ or } \text{ftype}(D.f) = \dots D' \dots \text{ and } E \ni v : D' \text{ and } D' \neq D}{E \vdash \top \Rightarrow A \parallel \langle s? p.f=v \rangle \Rightarrow B}$	<p>(PROC-ALLOCATED)</p> $\frac{E \vdash \diamond \quad E \ni p : D}{E \vdash p : D}$ <p>(PROC-LOCK)</p> $\frac{E \vdash \diamond \quad E \ni \ell : \text{Lock} \quad s \in \text{dom}(E)}{E \vdash \ell : \text{Lock}\{j; s\}}$ <p>(PROC-THREAD)</p> $\frac{E \ni s \text{ returns } aT \quad E \vdash_s M : aT}{E \vdash s[M]}$ <p>(PROC-NU)</p> $\frac{E, p : D, p \text{ returns } \square \text{Unit} \vdash A}{E \vdash (vp)A}$ <p>(PROC-SPECULATION)</p> $\frac{E \vdash A \quad E \vdash \phi \triangleright E' \quad E' \vdash B}{E \vdash \top \Rightarrow A \parallel \phi \Rightarrow B}$	<p>(PROC-RUNNABLE)</p> $\frac{E \vdash \diamond \quad p \in \text{dom}(E)}{E \vdash \text{runnable } p}$
<p>(PROC-FREE)</p> $\frac{E \vdash \diamond \quad E \ni p : D}{E \vdash \text{free } p}$ <p>(PROC-ATOMIC)</p> $\frac{E \vdash \diamond \quad E \ni \ell : \text{Atomic} \quad v \in \mathcal{B}' \cup \text{dom}(E)}{E \vdash \ell : \text{Atomic}\{v; j\}}$ <p>(PROC-CONDITION)</p> $\frac{E \vdash \diamond \quad E \ni p : \text{Cond} \quad E \ni \ell : \text{Lock} \quad \bar{s} \cup \bar{t} \subseteq \text{dom}(E)}{E \vdash p : \text{Cond}\{\ell; \bar{s}; \bar{t}\}}$ <p>(PROC-PAR)</p> $\frac{E \vdash A \quad E \vdash B}{E \vdash A \mid B}$ <p>(PROC-ACTION)</p> $\frac{E \vdash \alpha \triangleright E' \quad E' \vdash A}{E \vdash \alpha A}$ <p>(PROC-SPECULATION-SHAPE-ERROR)</p> $\frac{s \in \text{dom}(E) \quad E \ni p : D \quad E \vdash A \quad \text{Either } \text{ftype}(D.f) = \dots bt \dots \text{ and } \neg(v \text{ inhabits } bt) \text{ or } \text{ftype}(D.f) = \dots D' \dots \text{ and } E \ni v : D' \text{ and } D' \neq D}{E \vdash \top \Rightarrow A \parallel \langle s? p.f=v \rangle \Rightarrow B}$	<p>(PROC-ALLOCATED)</p> $\frac{E \vdash \diamond \quad E \ni p : D}{E \vdash p : D}$ <p>(PROC-LOCK)</p> $\frac{E \vdash \diamond \quad E \ni \ell : \text{Lock} \quad s \in \text{dom}(E)}{E \vdash \ell : \text{Lock}\{j; s\}}$ <p>(PROC-THREAD)</p> $\frac{E \ni s \text{ returns } aT \quad E \vdash_s M : aT}{E \vdash s[M]}$ <p>(PROC-NU)</p> $\frac{E, p : D, p \text{ returns } \square \text{Unit} \vdash A}{E \vdash (vp)A}$ <p>(PROC-SPECULATION)</p> $\frac{E \vdash A \quad E \vdash \phi \triangleright E' \quad E' \vdash B}{E \vdash \top \Rightarrow A \parallel \phi \Rightarrow B}$	<p>(PROC-RUNNABLE)</p> $\frac{E \vdash \diamond \quad p \in \text{dom}(E)}{E \vdash \text{runnable } p}$		

Judgment $E \vdash \sigma \triangleright E'$.

$$\begin{array}{c}
\text{(ACT-BEGIN)} \\
\frac{s \in \text{dom}(E) \quad p \in \text{dom}(E)}{E \vdash \langle s!p \rangle \triangleright E, s: \uparrow p \uparrow} \\
\text{(ACT-ACQUIRE)} \\
\frac{s \in \text{dom}(E) \quad E \ni \ell: \text{Lock} \\ E' = \{s: \zeta \mid E \ni \text{onacq } \Phi \zeta \text{ and } E \Vdash_s \Phi\}}{E \vdash \langle s! \ell: j \rangle \triangleright E, E', s: \text{lock } \ell, s: \text{effects}(\ell, E)} \quad j \text{ even} \\
\text{(ACT-RELEASE)} \\
\frac{s \in \text{dom}(E) \quad E \ni \ell: \text{Lock}}{E, s: \text{lock } \ell \vdash \langle s! \ell: j \rangle \triangleright E, \ell: \text{effects}(s, E)} \quad j \text{ odd} \\
\text{(ACT-}\square\text{-RACING-FINAL-WRITE)} \\
\frac{s \in \text{dom}(E) \quad E \ni p: D \quad \text{ftype}(D.f) \in \{S, \text{final } \square S\} \\ E \Vdash_s v: \square S}{E \vdash \langle s! p.f=v \rangle \triangleright E} \\
\text{(ACT-}\boxtimes\text{-FINAL-WRITE)} \\
\frac{s \in \text{dom}(E) \quad E \ni p: D \quad \text{ftype}(D.f) = \text{final } \boxtimes S \\ E \Vdash_s v: \boxtimes S}{E \vdash \langle s! p.f=v \rangle \triangleright E} \\
\text{(ACT-}\square\text{-GUARDED-WRITE)} \\
\frac{s \in \text{dom}(E) \quad E \ni p: D \quad \text{ftype}(D.f) = \square S \text{wrguard } \Psi \\ E \Vdash_s v: \square S \quad E \ni t: \uparrow p \uparrow \text{ implies } E \Vdash_s \Psi \{\uparrow p / \text{this}\}}{E \vdash \langle s! p.f=v \rangle \triangleright E} \\
\text{(ACT-}\boxtimes\text{-GUARDED-WRITE)} \\
\frac{s \in \text{dom}(E) \quad E \ni p: D \quad \text{ftype}(D.f) = \boxtimes S \text{wrguard } \Psi \\ E \Vdash_s v: \boxtimes S \quad E \ni t: \uparrow p \uparrow \text{ implies } E \Vdash_s \Psi \{\uparrow p / \text{this}\}}{E \vdash \langle s! p.f=v \rangle \triangleright E} \\
\text{(ACT-}\square\text{-RACING-FINAL-GUARDED-SPECULATION)} \\
\frac{s \in \text{dom}(E) \quad E \ni p: D \quad \text{ftype}(D.f) \in \{S, \dots \square S, \dots\} \\ E \Vdash_s v: \square S}{E \vdash \langle s? p.f=v \rangle \triangleright E} \\
\text{(ACT-}\boxtimes\text{-FINAL-SPECULATION)} \\
\frac{s \in \text{dom}(E) \quad E \ni p: D \quad \text{ftype}(D.f) = \text{final } \boxtimes S \\ E \Vdash_s v: \square S \\ E' = \{t: \uparrow v \uparrow \mid E \Vdash_t \uparrow p \uparrow\}}{E \vdash \langle s? p.f=v \rangle \triangleright E, E'} \\
\text{(ACT-}\boxtimes\text{-GUARDED-SPECULATION)} \\
\frac{s \in \text{dom}(E) \quad E \ni p: D \quad \text{ftype}(D.f) = \boxtimes S \text{rdguard } \Phi \\ E \Vdash_s v: \square S \\ E' = \{t: \uparrow v \uparrow \mid E \Vdash_t \Phi \{\uparrow p / \text{this}\}\}}{E \vdash \langle s? p.f=v \rangle \triangleright E, E', \text{onacq } (\Phi \{\uparrow p / \text{this}\}) (\uparrow v \uparrow)}
\end{array}$$

C.4 Supporting judgments

Define $\text{envlocks}(E)$ to return the set of values V such that $\text{lock } V$ occurs in E .

Judgment $E \vdash \diamond$.

$$\begin{array}{c}
\frac{}{\emptyset \vdash \diamond} \quad \frac{E \vdash \diamond \quad \chi \notin \text{dom}(E)}{E, \chi \vdash \diamond} \quad \frac{E \vdash \diamond \quad E \vdash D \quad p \notin \text{dom}(E)}{E, p: D \vdash \diamond} \\
\frac{E \vdash \diamond \quad E \vdash \zeta}{\overline{E}, \zeta \vdash \diamond} \\
\frac{E \vdash \diamond \quad E \vdash a \quad E \vdash T \quad x \notin \text{dom}(E)}{\overline{E}, x: aT \vdash \diamond} \\
\frac{E \vdash \diamond \quad E \vdash x: \square T \quad E \vdash F: T}{\overline{E}, \text{final } x = F \vdash \diamond} \\
\frac{E \vdash \diamond \quad V \in \text{dom}(E) \quad V \notin \text{envlocks}(E)}{E, \text{lock } V \vdash \diamond} \\
\frac{E \vdash \diamond \quad \{s, \ell\} \subseteq \text{dom}(E) \quad \ell \notin \text{envlocks}(E)}{\overline{E}, s: \text{lock } \ell} \\
\frac{E \vdash \diamond \quad p \in \text{dom}(E) \quad E \vdash \zeta}{\overline{E}, p: \zeta} \quad \frac{E \vdash \diamond \quad E \vdash \Phi \quad E \vdash \zeta}{\overline{E}, \text{onacq } \Phi \zeta}
\end{array}$$

Judgment $E \vdash T$.

$$\frac{}{\overline{E} \vdash \text{bt}} \quad \frac{d(\overline{\chi}) \text{ is defined} \quad E \vdash \overline{a} \quad |\overline{a}| = |\overline{\chi}|}{\overline{E} \vdash d(\overline{a})}$$

Judgment $E \vdash a$.

$$\frac{}{\overline{E} \vdash \boxtimes} \quad \frac{}{\overline{E} \vdash \square} \quad \frac{\chi \in \text{dom}(E)}{\overline{E} \vdash \chi}$$

Judgment $E \vdash F: T$.

$$\frac{E \vdash V: \square D \quad E \vdash F: D \quad \text{ftype}(D.f) = \text{final } aT}{\overline{E} \vdash V: D} \quad \frac{E \vdash F: D \quad \text{ftype}(D.f) = \text{final } aT}{\overline{E} \vdash F.f: T}$$

Judgment $E \vdash \Phi$.

$$\frac{}{\overline{E} \vdash \text{true}} \quad \frac{E \vdash F: D}{\overline{E} \vdash F} \quad \frac{E \vdash \Phi \quad E \vdash \Psi}{\overline{E} \vdash \Phi \&\&\Psi} \quad \frac{E \vdash \Phi \quad E \vdash \Psi}{\overline{E} \vdash \Phi \parallel \Psi}$$

Judgment $E \vdash \zeta$.

$$\frac{p \in \text{dom}(E)}{E \vdash \uparrow p \uparrow}$$

C.5 Judgments for contexts

The following judgment is used in proofs to connect contexts and environments. See Appendix D.3.

Judgment $E \vdash C \triangleright E'$.

$$\begin{array}{c}
\text{(CONTEXT-PAR-1)} \quad \text{(CONTEXT-PAR-2)} \\
\frac{E \vdash A \quad E \vdash C \triangleright E'}{E \vdash [A] \triangleright E'} \quad \frac{E \vdash A \quad E \vdash C \triangleright E'}{E \vdash A | C \triangleright E'} \\
\text{(CONTEXT-NU)} \\
\frac{E, p: D, p \text{ returns } \square \text{Unit} \vdash C \triangleright E'}{E \vdash (vp) C \triangleright E', p: D, p \text{ returns } \square \text{Unit}} \\
\text{(CONTEXT-ACTION)} \\
\frac{E \vdash \alpha \triangleright E' \quad E' \vdash C \triangleright E''}{E \vdash \alpha C \triangleright E''} \\
\text{(CONTEXT-SPECULATION-INITIAL)} \\
\frac{E \vdash C \triangleright E'' \quad E \vdash \phi \triangleright E' \quad E' \vdash B}{E \vdash \top \Rightarrow C \parallel \phi \Rightarrow B \triangleright E''} \\
\text{(CONTEXT-SPECULATION-FINAL)} \\
\frac{E \vdash A \quad E \vdash \phi \triangleright E' \quad E' \vdash C \triangleright E''}{E \vdash \top \Rightarrow A \parallel \phi \Rightarrow C \triangleright E''} \\
\text{(CONTEXT-SPECULATION-INITIAL-SHAPE-ERROR)} \\
\frac{s \in \text{dom}(E) \quad E \ni p: D \quad E \vdash C \triangleright E' \\ \text{Either } \text{ftype}(D.f) = \dots \text{bt} \dots \text{ and } \neg(v \text{ inhabits } \text{bt}) \\ \text{or } \text{ftype}(D.f) = \dots D' \dots \text{ and } E \ni v: D'' \text{ and } D'' \neq D'}{E \vdash \top \Rightarrow C \parallel \langle s? p.f=v \rangle \Rightarrow B \triangleright E'} \\
\text{(CONTEXT-SPECULATION-FINAL-SHAPE-ERROR)} \\
\frac{s \in \text{dom}(E) \quad E \ni p: D \quad E \vdash A \\ \text{Either } \text{ftype}(D.f) = \dots \text{bt} \dots \text{ and } \neg(v \text{ inhabits } \text{bt}) \\ \text{or } \text{ftype}(D.f) = \dots D' \dots \text{ and } E \ni v: D'' \text{ and } D'' \neq D'}{E \vdash \top \Rightarrow A \parallel \langle s? p.f=v \rangle \Rightarrow C \triangleright E}
\end{array}$$

D. Proofs

Soundness follows from progress and preservation. Recall the soundness theorem.

Definition (1) (Stuck thread). Thread s is *stuck* in A if there exists $C \llbracket s \llbracket E[M] \rrbracket \rrbracket = A$ such that *none* of the following hold.

- (1) s has terminated: M is a return statement and $\mathbb{E} = \llbracket - \rrbracket$;
- (2) s can reduce: $C \llbracket s \llbracket E[M] \rrbracket \rrbracket \rightarrow C' \llbracket s \llbracket M' \rrbracket \rrbracket$ for some C', M' ;
- (3) s has had a class cast exception: $M = \text{val } x = (D)v$; M' and C does not contain subterm $v: D$; or
- (4) s has had a null pointer exception: M is a method, end, read, write, conditional or synchronization statement with target null.
- (5) s is waiting for a lock, i.e., M is a synchronization statement or call to `Cond.await`; □

Theorem (2) (Soundness). *Suppose the class table is well typed and method `Main.main()` is defined. Suppose further that A is a speculation-free process that is reachable from the bootstrap process, that is*

$$(vm) m:\text{Main} \mid m[\text{m.main}();] \rightarrow \dots \rightarrow A.$$

Then A does not contain a stuck thread.

PROOF. By inspection, the bootstrap process is well-formed and well-typed under the empty environment.

As shown in Appendix B, well-formedness is preserved by reduction. By Proposition 30, proved in Appendix D.7 below, well-typedness is preserved by reduction.

By Proposition 21, proved in Appendix D.5 below, no well-formed and well-typed processes may contain a stuck thread. \square

To prove progress and preservation, we first introduce the notion of a *trap*, i.e., a context that contains a bad speculation. In Appendix D.3, we provide composition/decomposition results enabling us to go back and forth between contexts and process. We relate the dynamic and static definitions of happens-before in Appendix D.4. We state progress in Appendix D.5 and give a brief outline of the proof. We prove preservation in two parts, treating structural order in Appendix D.6 and reduction in Appendix D.7.

D.1 Traps

Define $\text{lockenv}(E, \mathbb{C})$ as follows.

$$\begin{aligned} \text{lockenv}(E, \llbracket - \rrbracket) &= E \\ \text{lockenv}(E, \mathbb{C} \mid A) &= \text{lockenv}(E, \mathbb{C}) \\ \text{lockenv}(E, A \mid \mathbb{C}) &= \text{lockenv}(E, \mathbb{C}) \\ \text{lockenv}(E, (vp) \mathbb{C}) &= \text{lockenv}(E, \mathbb{C}) \\ \text{lockenv}(E, \top \Rightarrow \mathbb{C} \parallel \phi \Rightarrow A) &= \text{lockenv}(E, \mathbb{C}) \\ \text{lockenv}(E, \top \Rightarrow A \parallel \phi \Rightarrow \mathbb{C}) &= \text{lockenv}(E, \mathbb{C}) \\ \text{lockenv}(E, \alpha \mathbb{C}) &= \text{lockenv}(E, \mathbb{C}) \quad \text{if } \alpha \text{ nonlock} \\ \text{lockenv}(E, \langle s! \ell : j \rangle \mathbb{C}) &= \text{lockenv}(E, s : \text{lock } \ell, \mathbb{C}) \quad \text{if } j \text{ even} \\ \text{lockenv}(E, s : \text{lock } \ell, \langle s! \ell : j \rangle \mathbb{C}) &= \text{lockenv}(E, \mathbb{C}) \quad \text{if } j \text{ odd} \end{aligned}$$

Define $\text{lockenv}(\mathbb{C}) = \text{lockenv}(\emptyset, \mathbb{C})$. Define $\mathbb{C} \Vdash_s \Phi$ as shorthand for $\text{lockenv}(\mathbb{C}) \Vdash_s \Phi$.

Definition 5 (Trap). A context \mathbb{C} is a *shape trap* if $\mathbb{C} \ni p : D$,

$$\mathbb{C} = \mathbb{C}' \parallel \top \Rightarrow A \parallel \langle s? p.f=v \rangle \Rightarrow \mathbb{C}''$$

and either

- $\text{ftype}(D.f) = \dots bt \dots$ and $\neg(v \text{ inhabits } bt)$, or
- $\text{ftype}(D.f) = \dots D' \dots$ and $\neg(E \ni v : D')$.

A context $\mathbb{C} = \mathbb{C}' \parallel \top \Rightarrow A \parallel \langle s? p.f=v \rangle \Rightarrow \mathbb{C}''$ is a *trap* if $\mathbb{C} \ni p : D$ and either it is a shape trap or

- $\text{ftype}(D.f) = \boxtimes T \text{ wrguard } \Psi$ and there exists $\mathbb{D} \parallel \mathbb{C}'' = \mathbb{C}''$ such that (a) $\neg(\mathbb{C}' \parallel \mathbb{D} \Vdash_s \Psi)$, and (b) for every $\mathbb{D}' \parallel \mathbb{D}'' = \mathbb{D}$, $\neg(\mathbb{C}' \parallel \mathbb{D}')$, s justifies speculation $p.f=v$. \square

The last possibility indicates that s has given up the locks required to justify the speculation and has not done the necessary write. A shape trap can always be typed using `PROC-SPECULATION-SHAPE-ERROR`.

Lemma 6 (Trap). *Suppose \mathbb{C} is a trap, $\emptyset \vdash \mathbb{C} \parallel s[M]$, and $\mathbb{C} \parallel s[M] \rightarrow \mathbb{C}' \parallel s[M']$. Then, \mathbb{C}' is a trap.*

PROOF. A trap can only be removed by `R-SPECULATION-CLOSE`. Assume for the sake of contradiction that there exists a trap that can be removed by `R-SPECULATION-CLOSE`. Thus, we have a speculation of the form $\top \Rightarrow A \parallel \langle s? p.f=v \rangle \Rightarrow \mathbb{C}$ such that $\mathbb{C}' \parallel \top \Rightarrow A \parallel \langle s? p.f=v \rangle \Rightarrow \mathbb{C} \rightarrow \mathbb{C}' \parallel \mathbb{C}$ and that (1) \mathbb{C}' , s justifies speculation $p.f=v$. As (1) holds, we know that there exists some i such that $\sigma_i = \langle s! p.f=v \rangle$. However, this would require (2) ($\text{ftype}(D.f) = \dots bt \dots$ and v inhabits bt) or ($\text{ftype}(D.f) = \dots D' \dots$ and $E \ni v : D'$), which contradict the first two premises of the definition of a trap.

For the third possibility, note that by typing rules for actions, s must hold the necessary locks to produce a justifying write action. s cannot acquire the locks to do so after the speculation, since in this case the write cannot leave the speculation (synchronization actions do not commute with speculation). So s must have acquired the necessary locks before the speculation, and

must perform the write before releasing the locks, as s cannot acquire them again and subsequently justify the speculation. But this case is exactly that prohibited by the third clause of the definition.

Therefore, the original assumption is false, and the lemma holds. \square

D.2 Weakening/Strengthening

The proofs of the following lemmas follow standard lines.

Lemma 7 (Weakening). *If $E \vdash A$, $E, E' \vdash \diamond$, and E' contains no locks, then $E, E' \vdash A$. Similarly for other judgments.*

Corollary 8. (a) *Assume $p \notin \text{fn}(\sigma)$ and $E \vdash \sigma \triangleright E'$. Then, $E, p : D \vdash \sigma \triangleright E', p : D$.*

(b) *Assume $E \vdash A$ and $E \vdash \alpha \triangleright E'$ such that A doesn't have any synchronization actions that overlap with α . Then, $E' \vdash A$.*

(c) *Assume $E \vdash A$ and $E \vdash \phi \triangleright E'$. Then, $E' \vdash A$. \square*

Lemma 9 (Strengthening). *If $E, x : bS \vdash M : aT$, and $x \notin M$, then $E \vdash M : aT$.*

If $E, p : D, p$ returns $\square \text{Unit} \vdash A$, and $p \notin A$, then $E \vdash A$.

Corollary 10. *Assume $p \notin \text{fn}(\sigma)$ and $E, p : D \vdash \sigma \triangleright E', p : D, E''$. Then, $E \vdash \sigma \triangleright E', E''$. \square*

Definition 11 (Sensible extension). Suppose environment E is well formed. We define E' to be a *sensible extension* of E if E' is well formed and there exists s_i, ℓ_i, F , and F' such that: $E = F, s_1 : \text{lock } \ell_1, \dots, s_n : \text{lock } \ell_n$ and $E' = F F'$. \square

Lemma 12 (Actions preserve wellformed environments). *Suppose $E \vdash \sigma \triangleright E'$ and $E \vdash \diamond$. Then of $E' \vdash \diamond$.*

PROOF. By induction on $E \vdash \sigma \triangleright E'$. \square

Lemma 13 (Actions sensibly extend environments). *Suppose $E \vdash \sigma \triangleright E'$. E' is a sensible extension of E .*

PROOF. We prove this by analysis of the $E \vdash \sigma \triangleright E'$ judgments. We first note that for `ACT- \square -RACING-FINAL-WRITE`, `ACT- \square -GUARDED-WRITE`, `ACT- \boxtimes -FINAL-WRITE`, `ACT- \boxtimes -GUARDED-WRITE`, and `ACT- \square -RACING-FINAL-GUARDED-SPECULATION` the result is immediate since $E' = E$. `ACT-BEGIN`,

`ACT- \boxtimes -GUARDED-SPECULATION` are almost equally trivial since they simply add to E , which clearly is a sensible extension. Letting $E = F, s : \text{lock } \ell$ and

$$E' = F, \ell : \text{effects}(s, E')$$

gives the desired result for `ACT-RELEASE`. \square

Lemma 14 (Transitivity of sensibly extended environments). *Suppose E' sensibly extends E and E'' sensibly extends E' . Then, E'' sensibly extends E .*

PROOF. Trivial from the definition of sensibly extended environments. \square

Lemma 15 (Sensible environment extensions preserve typability). *If E' sensibly extends E and $E \vdash M$, then $E' \vdash M$.*

PROOF. This follows from the definition of sensible extensions and from Lemma 7. \square

Note, the previous lemma doesn't hold for processes since removal of locks can invalidate it for processes.

D.3 Decomposition/Composition

Lemma 16 (Context decomposition). *Suppose $E \vdash \mathbb{C} \parallel A$ and \mathbb{C} is not a shape trap. Then $\exists E'$ such that $E \vdash \mathbb{C} \triangleright E'$ and $E' \vdash A$. Further, E' is a sensible extension of E .*

PROOF. We prove this by induction on the context, \mathbb{C} . First, let us note that as \mathbb{C} is not a shape trap, we know by the definition of shape trap that the context \mathbb{C}' derived from \mathbb{C} in the proof below is also not a shape trap.

Case $\mathbb{C} = [-]$: Immediate.

Case $\mathbb{C} = \mathbb{C}' \mid B$: By assumption and inversion, $\mathbb{C}' \llbracket A \rrbracket \mid B$ must be typeable by PROC-PAR, and therefore (1) $E \vdash B$ and $E \vdash \mathbb{C}' \llbracket A \rrbracket$. By induction, there exists E' such that (2) $E \vdash \mathbb{C}' \triangleright E'$, (3) $E' \vdash A$, and (4) E' is a sensible extension of E . The result holds by (3), (4), and applying CONTEXT-PAR-1 to (1) and (2).

Case $\mathbb{C} = B \mid \mathbb{C}'$: Similar.

Case $\mathbb{C} = (vp)\mathbb{C}'$: By assumption and inversion, $(vp)\mathbb{C}' \llbracket A \rrbracket$ must be typeable by PROC-NU, giving (1) $E, p: D, p \text{ returns } \square \text{ Unit} \vdash \mathbb{C}' \llbracket A \rrbracket$. By induction, therefore, there exists E' such that (2) $E, p: D, p \text{ returns } \square \text{ Unit} \vdash \mathbb{C}' \triangleright E'$, (3) $E' \vdash A$, and (4) E' is a sensible extension of E . The result holds by (3), (4), and applying CONTEXT-NU to (2).

Case $\mathbb{C} = \alpha\mathbb{C}'$: By assumption and inversion, $\alpha\mathbb{C}' \llbracket A \rrbracket$ must be typeable by PROC-ACTION, giving (1) $E \vdash \alpha \triangleright E'$ and (2) $E' \vdash \mathbb{C}' \llbracket A \rrbracket$. Further, as Lemma 13 holds, E' sensibly extends E . By induction on (2), there exists (3) E'' that sensibly extends E' such that (4) $E' \vdash \mathbb{C}' \triangleright E''$, and (5) $E'' \vdash A$. By the transitivity of sensibly extended environments, (6) E'' sensibly extends E . By (6), (5), and applying CONTEXT-ACTION to (1) and (4), we get the desired result.

Case $\mathbb{C} = \top \Rightarrow \mathbb{C}' \llbracket \phi \Rightarrow B \rrbracket$: By assumption and inversion, $\top \Rightarrow \mathbb{C}' \llbracket A \rrbracket \llbracket \phi \Rightarrow B \rrbracket$ must be typeable by PROC-SPECULATION, giving (1) $E \vdash \mathbb{C}' \llbracket A \rrbracket$, (2) $E \vdash \phi \triangleright E'$, and (3) $E' \vdash B$. By induction on (1), there exists (4) E'' that sensibly extends E such that (5) $E \vdash \mathbb{C}' \triangleright E''$ and (6) $E'' \vdash A$. By (4), (6), and applying CONTEXT-SPECULATION-INITIAL to (2), (3), and (5), we get the desired result. It should be noted that PROC-SPECULATION-SHAPE-ERROR and CONTEXT-SPECULATION-INITIAL-SHAPE-ERROR cannot be applied in this case since we assumed that \mathbb{C} was not a shape trap.

Case $\mathbb{C} = \top \Rightarrow B \llbracket \phi \Rightarrow \mathbb{C}' \rrbracket$: By assumption and inversion, $\top \Rightarrow B \llbracket \phi \Rightarrow \mathbb{C}' \llbracket A \rrbracket$ must be typeable by PROC-SPECULATION, giving (1) $E \vdash B$, (2) $E \vdash \phi \triangleright E'$, and (3) $E' \vdash \mathbb{C}' \llbracket A \rrbracket$. Further, as Lemma 13 holds, E' sensibly extends E . By induction on (3), there exists (4) E'' that sensibly extends E' such that (5) $E' \vdash \mathbb{C}' \triangleright E''$ and (6) $E'' \vdash A$. By the transitivity of sensibly extended environments, (7) E'' sensibly extends E . By (7), (6), and applying CONTEXT-SPECULATION-FINAL to (1), (2), and (5), we get the desired result. It should be noted that PROC-SPECULATION-SHAPE-ERROR and CONTEXT-SPECULATION-FINAL-SHAPE-ERROR cannot be applied in this case since we assumed that \mathbb{C} was not a shape trap.

This completes the possible cases for the context resulting in the lemma being true. \square

Lemma 17 (Context composition). *Suppose (a) $E \vdash \mathbb{C} \triangleright E'$ and either (b) \mathbb{C} is a shape trap or (c) $E' \vdash A$. Then $E \vdash \mathbb{C} \llbracket A \rrbracket$.*

PROOF. We prove this by induction on the context, \mathbb{C} .

Case $\mathbb{C} = [-]$: By assumption, \mathbb{C} is clearly not a shape trap and so it must be the case that (c) holds. The result is immediate from this.

In the remainder of the cases, it is the case that \mathbb{C} could be a shape trap, i.e. that (b) holds. If this is the case, then by definition of a shape trap, any context, \mathbb{C}' , derived from \mathbb{C} would also be a shape trap. So for the remainder of the proof we will assume that (b*) if \mathbb{C} is a shape trap, then \mathbb{C}' is a shape trap holds.

Case $\mathbb{C} = \mathbb{C}' \mid B$: By applying inversion and CONTEXT-PAR-1 to $E \vdash \mathbb{C} \triangleright E'$, we get (1) $E \vdash B$ and (2) $E \vdash \mathbb{C}' \triangleright E'$. We know by assumption that either (b*) or (c) holds. In either case, we can apply the inductive hypothesis to (2) since either \mathbb{C}' is a shape trap or $E' \vdash A$. This gives (3) $E \vdash \mathbb{C}' \llbracket A \rrbracket$. Applying PROC-PAR to (1) and (3) gives $E \vdash \mathbb{C}' \llbracket A \rrbracket \mid B$ and thus, we have the desired result.

Case $\mathbb{C} = B \mid \mathbb{C}'$: Similar except using CONTEXT-PAR-2 instead of CONTEXT-PAR-1.

Case $\mathbb{C} = (vp)\mathbb{C}'$: Since $E \vdash \mathbb{C} \triangleright E'$, and $\mathbb{C} = (vp)\mathbb{C}'$, $(a^*) E \vdash \mathbb{C} \triangleright E', p: D, p \text{ returns } \square \text{ Unit}$ holds, since $p: D, p \text{ returns } \square \text{ Unit}$ must be part of E' by CONTEXT-NU. Applying inversion and CONTEXT-NU to (a^*) , we get (1) $E, p: D, p \text{ returns } \square \text{ Unit} \vdash \mathbb{C}' \triangleright E''$. We know by assumption that either (b*) or (c) holds. In either case, we can apply the inductive hypothesis to (1) since either \mathbb{C}' is a shape trap or $E' \vdash A$. This gives (2) $E, p: D, p \text{ returns } \square \text{ Unit} \vdash \mathbb{C}' \llbracket A \rrbracket$. Applying PROC-NU, we get (3) $E \vdash (vp)\mathbb{C}' \llbracket A \rrbracket$, the desired result.

Case $\mathbb{C} = \alpha\mathbb{C}'$: By applying inversion and CONTEXT-ACTION to (a), we get (1) $E \vdash \alpha \triangleright E''$ and (2) $E'' \vdash \mathbb{C}' \triangleright E'$. We know by assumption that either (b*) or (c) holds. In either case, we can apply the inductive hypothesis to (2) since either \mathbb{C}' is a shape trap or $E' \vdash A$. This gives (3) $E'' \vdash \mathbb{C}' \llbracket A \rrbracket$. Applying PROC-ACTION to (1), and (3), we get (4) $E \vdash \alpha\mathbb{C}' \llbracket A \rrbracket$, the desired result.

Case $\mathbb{C} = \top \Rightarrow \mathbb{C}' \llbracket \phi \Rightarrow B \rrbracket$: There are two cases for which typing rule will type (a).

Case CONTEXT-SPECULATION-INITIAL: By applying inversion and CONTEXT-SPECULATION-INITIAL to (a), we get (1) $E \vdash \mathbb{C}' \triangleright E'$, (2) $E \vdash \phi \triangleright E''$, (3) $E'' \vdash B$. We know by assumption that either (b*) or (c) holds. In either case, we can apply the inductive hypothesis to (1) since either \mathbb{C}' is a shape trap or $E' \vdash A$. This gives (4) $E \vdash \mathbb{C}' \llbracket A \rrbracket$. Applying PROC-SPECULATION to (4), (2), and (3) gives $E \vdash \top \Rightarrow \mathbb{C}' \llbracket A \rrbracket \llbracket \phi \Rightarrow B \rrbracket$, as desired.

Case CONTEXT-SPECULATION-INITIAL-SHAPE-ERROR: By applying inversion and CONTEXT-SPECULATION-INITIAL-SHAPE-ERROR to (a), we get (1) $s \in \text{dom}(E)$, (2) $E \ni p: D$, (3) $E \vdash \mathbb{C}' \triangleright E'$, and (4) either $\text{ftype}(D.f) = \dots bt \dots$ and $\neg(v \text{ inhabits } bt)$ or $\text{ftype}(D.f) = \dots D' \dots$ and $E \ni v: D''$ and $D'' \neq D'$. We know by assumption that either (b*) or (c) holds. In either case, we can apply the inductive hypothesis to (3) since either \mathbb{C}' is a shape trap or $E' \vdash A$. This gives (5) $E \vdash \mathbb{C}' \llbracket A \rrbracket$. Applying PROC-SPECULATION-SHAPE-ERROR to (1), (2), (5), and (4) gives $E \vdash \top \Rightarrow \mathbb{C}' \llbracket A \rrbracket \llbracket \phi \Rightarrow B \rrbracket$, as desired.

Since CONTEXT-SPECULATION-INITIAL and CONTEXT-SPECULATION-INITIAL-SHAPE-ERROR are the only two judgments which could type (a) under the current assumption of what shape \mathbb{C} has and the lemma holds for both of these judgments, then the lemma holds for this case.

Case $\mathbb{C} = \top \Rightarrow B \llbracket \phi \Rightarrow \mathbb{C}' \rrbracket$: There are two cases for which typing rule will type (a).

Case CONTEXT-SPECULATION-FINAL: By applying inversion and CONTEXT-SPECULATION-FINAL to (a), we get (1) $E \vdash B$, (2) $E \vdash \phi \triangleright E''$, and (3) $E'' \vdash \mathbb{C}' \triangleright E'$. We know by assumption that either (b*) or (c) holds. In either case, we can apply the inductive hypothesis to (3) since either \mathbb{C}' is a shape trap or $E' \vdash A$. This gives (4) $E'' \vdash \mathbb{C}' \llbracket A \rrbracket$. Applying PROC-SPECULATION to (1), (2), and (4) gives $E \vdash \top \Rightarrow B \llbracket \phi \Rightarrow \mathbb{C}' \llbracket A \rrbracket$, as desired.

Case CONTEXT-SPECULATION-FINAL-SHAPE-ERROR: By applying inversion and CONTEXT-SPECULATION-FINAL-SHAPE-ERROR to (a), we get (1) $s \in \text{dom}(E)$, (2) $E \ni p: D$, (3) $E \vdash B$, (4) either $\text{ftype}(D.f) = \dots bt \dots$ and $\neg(v \text{ inhabits } bt)$ or $\text{ftype}(D.f) = \dots D' \dots$ and $E \ni v: D''$ and $D'' \neq D'$. Applying PROC-SPECULATION-SHAPE-ERROR to (1), (2), (3), and (4) gives $E \vdash \top \Rightarrow B \llbracket \phi \Rightarrow \mathbb{C}' \llbracket A \rrbracket$, as desired.

Since CONTEXT-SPECULATION-FINAL and CONTEXT-SPECULATION-FINAL-SHAPE-ERROR are the only two judgments which could type (a) under the current assumption of what shape \mathbb{C} has and the lemma holds for both of these judgments, then the lemma holds for this case.

As we have proved all cases, we have proved the lemma. \square

D.4 Happens-before

In non-trap processes, the type system exactly captures the happens-before relation.

Lemma 18 (Happens-before). *Suppose $\emptyset \vdash \mathbb{C} \triangleright E$.*

- (a) *If $\langle t!p \rangle <_{hb}^{\mathbb{C}, s} s[-]$ then $E \ni (s: \lambda p)$.*
- (b) *If \mathbb{C} is not a trap and $E \ni (s: \lambda p)$ then $\langle t!p \rangle <_{hb}^{\mathbb{C}, s} s[-]$.*

PROOF. By induction on \mathbb{C} . The interesting cases are for speculation, and these follow from the definitions of trap and of justified speculation. \square

Lemma 19 (Justified read). *Suppose (a) $\emptyset \vdash \mathbb{C} \triangleright E$, (b) \mathbb{C}, s justifies read $p.f = v$ and (c) $E \ni p: D$.*

- *If (d1) $\text{ftype}(D.f) = \dots \square T \dots$ then either (f1) \mathbb{C} is a shape trap or (g1) $E \vdash_3 v: \square T$.*
- *If (d2) $\text{ftype}(D.f) = \text{final} \boxtimes T$ then either (f2) \mathbb{C} is a shape trap or (g2) $E \vdash_3 v: \boxtimes T$.*

- If $(d3) \text{ftype}(D.f) = \boxtimes T \text{rdguard } \Phi$ and $(e3) E \Vdash_s \Phi$, then either $(f3) \mathbb{C}$ is a shape trap or $(g3) E \vdash_s v : \boxtimes T$.

PROOF. The first case is immediate from the definitions.

The other cases are immediate unless the read is justified by a speculation. Then we must show that $E \ni (s : \uparrow v)$.

In the case of a final field, the result is immediate from the typing rule for final speculations.

In the case of a lock-protected field, the result follows from the fact that s holds the locks necessary to read the field. By the typing rule for lock-protected speculations (and lock acquisition) the effect is guaranteed to be there. \square

It is worth noting that the environment $E' = \{t : \uparrow v \mid E \Vdash_t \Phi\{\!|p/\text{this}\!|\}\}$ in ACT- \boxtimes -GUARDED-SPECULATION could be restricted to $t \neq s$, since s cannot read the speculated value. But this is not necessary.

For a speculation about a write by s on a lock-protected field, it is only possible for the speculation to finalize if it is justified before the speculation or if s holds the necessary locks at the point of speculation. If s acquires a lock inside a speculation, then the commuting rules do not allow any subsequent write to justify the speculation.

Lemma 20 (Justified speculation). Suppose $\emptyset \vdash \mathbb{C} \triangleright E$ and $E \vdash \phi \triangleright E'$ and $E' \vdash A$. If \mathbb{C}, s justifies speculation ϕ then $E \vdash A$.

PROOF. In general, E' extends E by adding immediate and latent effects. Otherwise the environments are the same.

For final fields, only one write can possibly justify the speculation, and this write happens-before $\uparrow p$. If thread writing $p.f$ had $\uparrow v$, then so will any thread that has $\uparrow p$.

For lock-protected fields, the justifying write must occur while holding $\uparrow v$ and the relevant write locks. Any reader must have the corresponding read locks. Since these must overlap, one can reason, via the definition of $\emptyset \vdash \mathbb{C} \triangleright E$, that any occurrence of v has the required effect. \square

D.5 Progress

Recall the definition of a stuck thread

Definition (1) (Stuck thread). Thread s is *stuck* in A if there exists $\mathbb{C} \llbracket s \llbracket \mathbb{E}[M] \rrbracket \rrbracket = A$ such that none of the following hold.

- (1) s has terminated, i.e., M is a return statement and $\mathbb{E} = \llbracket - \rrbracket$;
- (2) s can reduce, i.e., $\mathbb{C} \llbracket s \llbracket \mathbb{E}[M] \rrbracket \rrbracket \rightarrow \mathbb{C}' \llbracket s \llbracket M' \rrbracket \rrbracket$ for some \mathbb{C}' and M' ;
- (3) s is waiting for a lock, i.e., M is a synchronization statement or call to `Cond.await`;
- (4) s has had a class cast exception, i.e., $M = \text{val } x = (D)v$; M' and \mathbb{C} does not contain subterm $v:D$; or
- (5) s has had a null pointer exception, i.e., M is a method, end, read, write, conditional or synchronization statement with target `null`. \square

Proposition 21 (Progress). If A contains no speculation and $\emptyset \vdash A$ then A contains no stuck thread.

PROOF. For a contradiction, suppose that s is stuck in A and $A = \mathbb{C} \llbracket s \llbracket M \rrbracket \rrbracket$.

Note that \mathbb{C} cannot be a trap, since it contains no speculation.

Applying decomposition and inversion to the supposition, we have that there exists E, a and T such that (1) $\emptyset \vdash \mathbb{C} \triangleright E$ and (2) $E \vdash_s M : aT$. By induction on judgment (2), one can show a contradiction for any M .

The only unusual case is R-END, and this follows from Lemma 18. \square

D.6 Structural order preserves typing

We begin with some lemmas.

Lemma 22 (Action permutation). Let $\vec{\sigma} \triangleright \vec{\tau}$ where $s \llbracket - \rrbracket \notin \vec{\sigma}$ and $s \llbracket - \rrbracket \notin \vec{\tau}$ and $E \vdash \vec{\sigma} \triangleright E'$. Then, $E \vdash \vec{\tau}A$.

PROOF. By induction on the definition of the \triangleright precongruence. \square

Lemma 23 (Action-speculation permutation). If $\phi \alpha \triangleright \alpha \phi$ or $\text{thrd}(\phi) \neq \text{thrd}(\alpha)$, and $E \vdash \phi \triangleright E', E' \vdash \alpha \triangleright E''$, and $E \vdash \alpha \triangleright E''$, then $E'' \vdash \phi \triangleright E'''$.

PROOF. This can be proven by induction on the \triangleright precongruence and a case analysis of α and ϕ . \square

Lemma 24 (Substitution). 1. If $E, x:D \vdash M : C$ and $E \vdash v : D$, then $E \vdash M\{\!|v/x\!|\} : C$.

2. Assume $E \vdash p : cD$, $\text{mtype}(D.m) = c;\vec{b}\vec{S} \rightarrow bS$, $E \vdash \vec{V} : \vec{b}\vec{S}$, $E, x:bS \vdash M : aT$, $E \vdash W : bS$, and W is the result of $p.m(\vec{V})$. Then $E \vdash M\{\!|W/x\!|\} : aT$.

3. Assume $E \vdash p : cD$, $\text{mtype}(D.m) = c;\vec{b}\vec{S} \rightarrow bS$, $\text{mbody}(D.m) = \lambda\vec{y}.M$, and $E \vdash \vec{V} : \vec{b}\vec{S}$. Then $E \vdash M\{\!|p/\text{this}\!|\}\{\!|\vec{V}/\vec{y}\!|\} : aT$.

PROOF. Each of these can be proved using induction on $E \vdash M : C$ \square

We now explicitly state the rules for defining $A \geq B$ as the least precongruence on processes that satisfy the axioms in Equation 6.1. They are as follows:

$$\begin{array}{c}
\begin{array}{cc}
\text{(S-REFLEXIVITY)} & \text{(S-TRANSITIVITY)} & \text{(S-CTXT-ACTION)} \\
A \geq A & A \geq A' \quad A' \geq B & A \geq A' \\
& A \geq B & \alpha A \geq \alpha A'
\end{array} \\
\begin{array}{ccc}
\text{(S-CTXT-PAR1)} & \text{(S-CTXT-PAR2)} & \text{(S-CTXT-NU)} \\
\frac{A \geq A'}{A|B \geq A'|B} & \frac{B \geq B'}{A|B \geq A|B'} & \frac{A \geq A'}{(vp)A \geq (vp)A'}
\end{array} \\
\begin{array}{cc}
\text{(S-CTXT-SPECULATION1)} & \text{(S-CTXT-SPECULATION2)} \\
\frac{A \geq A'}{\top \Rightarrow A \llbracket \phi \rrbracket \Rightarrow B \geq \top \Rightarrow A' \llbracket \phi \rrbracket \Rightarrow B} & \frac{B \geq B'}{\top \Rightarrow A \llbracket \phi \rrbracket \Rightarrow B \geq \top \Rightarrow A \llbracket \phi \rrbracket \Rightarrow B'}
\end{array}
\end{array}$$

Proposition 25 (Structural order preserves typing). If $E \vdash A$, and $A \geq B$, then $E \vdash B$.

PROOF. Assume that $E \vdash A$, and $A \geq B$. We prove this by induction on the structural order rules found in Equation 6.1 and on the ones explicitly stated above. For each case, we then use induction on the typing rules which are valid for that structural order.

Case S-REFLEXIVITY:

Assume that $A = B$. Therefore, the results holds trivially.

Case S-TRANSITIVITY:

By inversion on S-TRANSITIVITY, we get (1) $A \geq A'$ and (2) $A' \geq B$ for all A' . By the induction hypothesis on (1) and since $E \vdash A$, (3) $E \vdash A'$.

Now this, together with the induction hypothesis on (2), means that (4) $E \vdash B$, as desired.

Case S-CTXT-...

In each case, the result follows using inversion and induction.

Case S-NU-FREE (\rightarrow):

Assume that $B = A|(vp)(p:d\langle\vec{y}\rangle|\text{free } p)$. We can further safely assume that (a) $d\langle\vec{x}\rangle$ is defined, (b) $p \notin \text{dom}(E)$, and that (c) $|\vec{y}| = |\vec{x}|$. Otherwise, we can simply pick a different p and d which do satisfy these assumptions. We know by assumption that $E \vdash A$, so (1) $E \vdash \diamond$. By the rules for typing $E \vdash \boxtimes$ and $E \vdash \square$, we know that (2) $E \vdash \vec{y}$. By (a), (2), and (c), we know that (3) $E \vdash d\langle\vec{y}\rangle$ holds. As we have (1), (3), and (b), we know that (4) $E, p:d\langle\vec{y}\rangle, p \text{ returns } \square \text{Unit} \vdash \diamond$ holds. Further, by applying PROC-FREE to (1) and (4), we have (5) $E, p:d\langle\vec{y}\rangle, p \text{ returns } \square \text{Unit} \vdash \text{free } p$. We can also apply PROC-ALLOCATED to (1) and (4), giving us (6) $E, p:d\langle\vec{y}\rangle, p \text{ returns } \square \text{Unit} \vdash p:d\langle\vec{y}\rangle$. By applying PROC-PAR to (6) and (5), we get (7) $E, p:d\langle\vec{y}\rangle, p \text{ returns } \square \text{Unit} \vdash (p:d\langle\vec{y}\rangle|\text{free } p)$. By applying PROC-NU to (7), we get (8) $E \vdash (vp)(p:d\langle\vec{y}\rangle|\text{free } p)$. Finally, by applying PROC-PAR to $E \vdash A$ and (8), we get (9) $E \vdash A|(vp)(p:d\langle\vec{y}\rangle|\text{free } p)$, as desired.

Case S-NU-FREE (\leftarrow):

Assume that $A = A'|(vp)(p:d\langle\vec{y}\rangle|\text{free } p)$ and $B = A'$. We know by assumption that $E \vdash A$. By applying inversion and PROC-PAR on $E \vdash A$, we get (1) $E \vdash A'$ and (2) $E \vdash (vp)(p:d\langle\vec{y}\rangle|\text{free } p)$. The result holds from (1).

Case S-NU-PAR (\rightarrow):

Assume $A = (vp)(B'|A')$ and $B = B'|((vp)A')$. By inversion and PROC-NU on $E \vdash A$, we have (1) $E, p:D, p \text{ returns } \square \text{Unit} \vdash (B'|A')$. By inversion and PROC-PAR on (1), we have (2) $E, p:D, p \text{ returns } \square \text{Unit} \vdash B'$ and (3) $E, p:D, p \text{ returns } \square \text{Unit} \vdash A'$. Now, by assumption of S-NU-PAR, we know that $p \notin \text{fn}(B')$. Thus, we can apply Corollary 10 to (2), giving (4) $E \vdash B'$. By applying PROC-NU to (3), we get (5) $E \vdash ((vp)A')$. Applying PROC-PAR to (4) and (5) gives $E \vdash B'|((vp)A')$, as desired.

Case S-NU-PAR (\leftarrow):

Similar except use Corollary 8 instead of Corollary 10.

Case S-PAR-PAR (\rightarrow):

Case S-PAR-PAR (\leftarrow):

Case S-PAR (\rightarrow):

Case S-PAR (\leftarrow):

Trivial through application of inversion and PROC-PAR.

Case S-NU-NU (\rightarrow):

Case S-NU-NU (\leftarrow):

Trivial through application of inversion and PROC-NU, Lemma 7, Corollary 8, Lemma 9, and Corollary 10.

Case S-NU-PREFIX (\rightarrow):

Let $A = (vp)\alpha A'$ and $B = \alpha(vp)A'$ such that $p \notin \text{fn}(\alpha)$. Applying inversion and PROC-NU to $E \vdash A$ gives (1) $E, p:D, p \text{ returns } \square \text{Unit} \vdash \alpha A'$. Applying inversion and PROC-ACTION to (1) gives (2) $E, p:D, p \text{ returns } \square \text{Unit} \vdash \alpha \triangleright E'$, where $E' = E, p:D, p \text{ returns } \square \text{Unit}, E''$, and (3) $E' \vdash A'$. Applying Corollary 8 followed by Corollary 10 to (2) gives (4) $E \vdash \alpha \triangleright E, E''$, which in turn means that (5) $E' = E, E'', p:D, p \text{ returns } \square \text{Unit}$. Thus, we have (6) $E, E'', p:D, p \text{ returns } \square \text{Unit} \vdash A'$, by substituting in for E' in (3). Applying PROC-NU to (6) gives (7) $E, E'' \vdash (vp)A'$. Applying PROC-ACTION on (4) and (7) gives $E \vdash \alpha(vp)A'$, the desired result.

Case S-NU-PREFIX (\leftarrow):

Similar.

Case S-PREFIX-PAR:

Let $A = A' | (\alpha A'')$ and $B = \alpha(A' | A'')$. We know by applying inversion and PROC-PAR to A that (1) $E \vdash A'$ and (2) $E \vdash \alpha A''$ hold. Applying inversion and PROC-ACTION to (2) gives (3) $E \vdash \alpha \triangleright E'$ and (4) $E' \vdash A''$. We know by the well-formedness of A that \mathbb{C} is contiguous. Thus, we know that if α is a synchronization action, then it can't overlap with A . Thus, by Corollary 8, we have (5) $E' \vdash A$. Applying inversion and PROC-PAR to (5) gives (6) $E' \vdash A'$ and (7) $E' \vdash \alpha A''$. Applying PROC-PAR to (6) and (4) gives (8) $E' \vdash A' | A''$. Applying PROC-ACTION to (3) and (8) gives (9) $E \vdash \alpha(A' | A'')$, as desired.

Case S-PREFIX:

Let $A = \bar{\alpha}A'$ and $B = \bar{\beta}A'$ such that $\bar{\alpha} \triangleright \bar{\beta}$. We know by assumption that $E \vdash A$. By successive applications of inversion and PROC-ACTION, we have (1) $E \vdash \bar{\alpha} \triangleright E'$ and (2) $E' \vdash A'$. By applying Lemma 22, we have (3) $E \vdash \bar{\beta}A'$, as desired.

Case S-PREFIX-SPECULATION:

Let $A = \top \Rightarrow (\alpha A') \parallel \phi \Rightarrow A''$ and $B = \alpha(\top \Rightarrow A' \parallel \phi \Rightarrow A'')$ such that (1) $\phi \alpha \triangleright \alpha \phi$ or (2) $\text{thrd}(\phi) \neq \text{thrd}(\alpha)$. As $E \vdash A$, there are two typing rules which can be applied here, PROC-SPECULATION and PROC-SPECULATION-SHAPE-ERROR. We consider both cases to complete the proof of this case.

Case PROC-SPECULATION:

Assume that PROC-SPECULATION types $E \vdash A$. By inversion and PROC-SPECULATION, we have (3a) $E \vdash \alpha A'$, (4a) $E \vdash \phi \triangleright E'$, and (5a) $E' \vdash \alpha A''$. Applying inversion and PROC-ACTION to (3a) gives (6a) $E \vdash \alpha \triangleright E''$ and (7a) $E'' \vdash A'$. Applying inversion and PROC-ACTION to (5a) gives (8a) $E' \vdash \alpha \triangleright E'''$ and (9a) $E''' \vdash A''$. Applying Lemma 23 to (1), (2), (4a), (8a), and (6a) gives (11a) $E'' \vdash \phi \triangleright E'''$. Applying PROC-SPECULATION to (7a), (11a), and (7a) gives (12a) $E'' \vdash \top \Rightarrow A' \parallel \phi \Rightarrow A''$. Applying PROC-ACTION to (6a) and (12a) gives $E \vdash \alpha(\top \Rightarrow A' \parallel \phi \Rightarrow A'')$, as desired.

Case PROC-SPECULATION-SHAPE-ERROR:

Assume that PROC-SPECULATION-SHAPE-ERROR types $E \vdash A$. Thus, $\phi = \langle s?p.f=v \rangle$. By inversion and PROC-SPECULATION-SHAPE-ERROR, we have (3b) $s \in \text{dom}(E)$, (4b) $E \ni p:D$, (5b) either $(\text{ftype}(D.f) = \dots bt \dots$ and $\neg(v \text{ inhabits } bt)$) or $(\text{ftype}(D.f) = \dots D' \dots$ and $E \ni v:D'$ and $D'' \neq D')$ and (6b) $E \vdash \alpha A'$. Applying inversion and PROC-ACTION to (6b) gives (7b) $E \vdash \alpha \triangleright E'$ and (8b) $E' \vdash A'$. It is clear that if (3b), (4b), and (5b) hold, then (9b) $s \in \text{dom}(E')$, (10b) $E' \ni p:D$, and (11b) either $(\text{ftype}(D.f) = \dots bt \dots$ and $\neg(v \text{ inhabits } bt)$) or $(\text{ftype}(D.f) = \dots D' \dots$ and $E' \ni v:D''$ and $D'' \neq D')$ also must hold. Therefore, we can apply PROC-SPECULATION-SHAPE-ERROR to (9b), (10b), (8b), and (11b) to get (12b) $E' \vdash \top \Rightarrow A' \parallel \phi \Rightarrow A''$. Applying PROC-ACTION to (7b) and (12b) gives (13b) $E \vdash \alpha(\top \Rightarrow A' \parallel \phi \Rightarrow A'')$, as desired.

Since we showed that for both cases of typing $E \vdash A$ results in $E \vdash B$ being typed, we have shown that this case is satisfied as well.

Case S-NU-SPECULATION (\rightarrow):

Let $A = (vp)(\top \Rightarrow A' \parallel \phi \Rightarrow A'')$ and $B = \top \Rightarrow ((vp)A') \parallel \phi \Rightarrow ((vp)A'')$ such that $p \notin \text{fn}(\phi)$. Applying inversion and PROC-NU to $E \vdash A$ gives (1) $E, p:D, p \text{ returns } \square \text{Unit} \vdash \top \Rightarrow A' \parallel \phi \Rightarrow A''$. There are two typing rules which can be applied here, PROC-SPECULATION and PROC-SPECULATION-SHAPE-ERROR. We consider both cases to complete the proof of this case.

Case PROC-SPECULATION:

Applying inversion and PROC-SPECULATION to (1) gives (2a) $E, p:D, p \text{ returns } \square \text{Unit} \vdash A'$, (3a) $E, p:D, p \text{ returns } \square \text{Unit} \vdash \phi \triangleright E, p:D, p \text{ returns } \square \text{Unit}, E'$, and (4a) $E, p:D, p \text{ returns } \square \text{Unit}, E' \vdash A''$. Applying Corollary 8 followed by Corollary 10 to (3a) gives (5a) $E \vdash \phi \triangleright E, E'$ and (6a) $E, p:D, p \text{ returns } \square \text{Unit} \vdash \phi \triangleright E, E', p:D, p \text{ returns } \square \text{Unit}$, which means that (7a) $E, p:D, p \text{ returns } \square \text{Unit}, E' = E, E', p:D, p \text{ returns } \square \text{Unit}$ holds as well. Substituting in (4a) gives (8a) $E, E', p:D, p \text{ returns } \square \text{Unit} \vdash A''$. Applying PROC-NU to both (2a) and (8a) gives (9a) $E \vdash (vp)A'$ and (10a) $E, E' \vdash (vp)A''$, respectively. Applying PROC-SPECULATION to (9a), (5a), and (10a) gives (11a) $E \vdash \top \Rightarrow ((vp)A') \parallel \phi \Rightarrow ((vp)A'')$, as desired.

Case PROC-SPECULATION-SHAPE-ERROR:

Applying inversion and PROC-SPECULATION-SHAPE-ERROR to (1) where $\phi = \langle s?q.f=v \rangle$ gives (2a) $s \in \text{dom}(E, p:D, p \text{ returns } \square \text{Unit})$, (3a) $E, p:D, p \text{ returns } \square \text{Unit} \ni q:D$ with $p \neq q$, (4a) either $(\text{ftype}(D.f) = \dots bt \dots$ and $\neg(v \text{ inhabits } bt)$) or $(\text{ftype}(D.f) = \dots D' \dots$ and $E, p:D, p \text{ returns } \square \text{Unit} \ni v:D''$ and $D'' \neq D')$ and (5a) $E, p:D, p \text{ returns } \square \text{Unit} \vdash A'$. Applying PROC-NU to (5a) gives (6a) $E \vdash (vp)A'$. Applying PROC-SPECULATION-SHAPE-ERROR to (2a), (3a), (6a), and (4a) gives (7a) $E \vdash \top \Rightarrow ((vp)A') \parallel \phi \Rightarrow ((vp)A'')$, as desired. Note, we can just put in the (vp) before A'' in the speculation since the speculation side is not typed by PROC-SPECULATION-SHAPE-ERROR.

Case S-NU-SPECULATION (\leftarrow):

Similar, but reversing the steps.

Case S-PAR-SPECULATION (\rightarrow):

Similar to the S-NU-SPECULATION (\rightarrow) case with the exception of using PROC-PAR instead of PROC-NU.

Case S-PAR-SPECULATION (\leftarrow):

Similar to the S-NU-SPECULATION (\leftarrow) case with the exception of using PROC-PAR instead of PROC-NU.

As we have proved all cases, we have proved the proposition. \square

D.7 Reduction preserves typing

Lemma 26 (Shape traps preserve typability). *If (1) $\emptyset \vdash \mathbb{C} \llbracket s[M] \rrbracket$, (2) \mathbb{C} is a shape trap, and (3) $\mathbb{C} \llbracket s[M] \rrbracket \rightarrow \mathbb{C}' \llbracket s[M'] \rrbracket$, then $\emptyset \vdash \mathbb{C}' \llbracket s[M'] \rrbracket$.*

PROOF. Let us assume (1), (2), and (3). Then we know that \mathbb{C}' is also a shape trap since traps can never be removed. So, by the definition of a shape trap, $\emptyset \vdash \mathbb{D} \triangleright E$ and $\mathbb{C}' = \mathbb{D} \llbracket \top \Rightarrow A' \parallel \langle s?p.f=v \rangle \Rightarrow \mathbb{D}' \rrbracket$ where (4) $\top \Rightarrow A' \parallel \langle s?p.f=v \rangle \Rightarrow \mathbb{D}'$ is a shape trap with respect to E . By the definition of a shape trap on (4), (5) $E \vdash \top \Rightarrow A' \parallel \langle s?p.f=v \rangle \Rightarrow \mathbb{D}'$. By applying CONTEXT-SPECULATION-FINAL-SHAPE-ERROR to the premises gotten by applying inversion and PROC-SPECULATION-SHAPE-ERROR to (5), we have (6) $E \vdash \top \Rightarrow A' \parallel \langle s?p.f=v \rangle \Rightarrow \mathbb{D}' \triangleright E$. Combining (6) with $\emptyset \vdash \mathbb{D} \triangleright E$, gives (7) $\emptyset \vdash \mathbb{C}' \triangleright E$. By applying Lemma 17 on (7) and the fact that \mathbb{C}' is a shape trap, we get (8) $\emptyset \vdash \mathbb{C}' \llbracket s[M'] \rrbracket$, as desired. \square

Lemma 27. (Thread reduction introduces a shape trap or sensibly extends the environment). *If $\emptyset \vdash \mathbb{C} \llbracket s[M] \rrbracket$, $\mathbb{C} \llbracket s[M] \rrbracket \rightarrow \mathbb{C}' \llbracket s[M'] \rrbracket$, and $\emptyset \vdash \mathbb{C} \triangleright E$, then either $(\emptyset \vdash \mathbb{C}' \triangleright E')$ and E' sensibly extends E) or \mathbb{C}' is a shape trap.*

PROOF. If \mathbb{C} is a shape trap, we are done since \mathbb{C}' must also be a shape trap since traps can never be removed. If a shape trap is introduced to \mathbb{C}' during the reduction step, we are again done. Therefore the interesting part of the proof is when \mathbb{C} and \mathbb{C}' are not shape traps. This can be proven by induction on reduction rules found in Equation 6.1, Equation 6.1, and Figure 6. \square

Lemma 28 (Thread return type existence). *If $E \vdash s[M]$, then $E = E', s \text{ returns } aT$, for some E' .*

PROOF. Assume $E \vdash s[M]$. Applying inversion and PROC-THREAD gives (1) $E \ni s$ returns aT and (2) $E \vdash_s M : aT$. Since we have (1) we can reorder E so that $E = E', s$ returns aT , as desired. \square

Lemma 29 (Thread return types). *If $(vs) \in \mathbb{C}$, then if $E \vdash \mathbb{C} \triangleright E', s$ returns aT , then $E \vdash \mathbb{C} \triangleright E', s$ returns bS .*

If $(vs) \notin \mathbb{C}$, then if E, s returns $aT \vdash \mathbb{C} \triangleright E', s$ returns aT , then E, s returns $bS \vdash \mathbb{C} \triangleright E', s$ returns bS .

PROOF. The first case follows from CONTEXT-NU. The second case follows from the fact that \mathbb{C} doesn't modify the return type of a thread. \square

We now state and prove preservation, also known as subject reduction.

Proposition 30 (Preservation). *If $\emptyset \vdash A$, and $A \rightarrow B$, then $\emptyset \vdash B$.*

PROOF. Assume that (1) $\emptyset \vdash A$ and (2) $A \rightarrow B$. We prove this by induction on the reduction rules found in Equation 6.1, Equation 6.1, and Figure 6. That is, we prove this by induction on (2). We start with the case of R-STRUCTURAL-ORDER.

Case R-STRUCTURAL-ORDER: Assume that R-STRUCTURAL-ORDER satisfies (2). Then by assumption, we know that (3) $A \geq A'$, (4) $A' \rightarrow B'$, and (5) $B' \geq B$. Applying Proposition 25 on (1) and (3) gives (6) $\emptyset \vdash A'$. Applying the inductive hypothesis on (6) and (4) gives (7) $\emptyset \vdash B'$. Applying Proposition 25 on (7) and (5) gives (8) $\emptyset \vdash B$, as desired.

We partition the remaining cases into two sets that we need to consider. One set of cases is comprised of the reduction rules which modify the context while reducing, namely R-METHOD-CONTEXT, and R-LOCK-CONTEXT. The other set consists of all remaining cases and do not modify the context. Let us first consider the cases for the rules which modify the context, as they are more interesting.

Case R-METHOD-CONTEXT: Assume that R-METHOD-CONTEXT satisfies (2). So, (3) $A = \mathbb{C} \llbracket s[\text{val } x =_s \{N\}M] \rrbracket$, (4) $B = \mathbb{C}' \llbracket s[\text{val } x =_s \{N'\}M] \rrbracket$. By the equalities of (3) and (4) combined with (2), we have (5) $\mathbb{C} \llbracket s[\text{val } x =_s \{N\}M] \rrbracket \rightarrow \mathbb{C}' \llbracket s[\text{val } x =_s \{N'\}M] \rrbracket$, and (6) $\mathbb{C} \llbracket s[N] \rrbracket \rightarrow \mathbb{C}' \llbracket s[N'] \rrbracket$. Now there are two cases to consider. Either \mathbb{C} is a shape trap or it isn't. If it is, then by Lemma 26, $\emptyset \vdash \mathbb{C}' \llbracket s[N'] \rrbracket$. Now since $B = \mathbb{C}' \llbracket s[N'] \rrbracket$, we have $\emptyset \vdash B$, as desired. Thus, if \mathbb{C} is a shape trap, we are done. We proceed assuming that \mathbb{C} is not a shape trap. By applying Lemma 16 on (1) and (3), we have (7) $\emptyset \vdash \mathbb{C} \triangleright E$, (8) $E \vdash s[\text{val } x =_s \{N\}M]$, and (9) E is a sensible extension of \emptyset . By applying inversion and PROC-THREAD on (8), we get (10) $E \ni s$ returns aT and (11) $E \vdash_s \text{val } x =_s \{N\}M : aT$. Let (12) E', s returns aT be a reordering of E . Further, by using inversion and STAT-METHOD-CONTEXT on (11), we know that (13) E', s returns $bS \vdash_s N : bS$ and (14) E', s returns $aT, x : bS \vdash_s M : aT$. Using PROC-THREAD on (13) gives (15) E', s returns $bS \vdash s[N]$. Substituting (12) into (3), gives (16) $\emptyset \vdash \mathbb{C} \triangleright E', s$ returns aT . Now it must be the case that $(vs) \in \mathbb{C}$ since it clearly can't be in \emptyset .

Applying Lemma 29 to (16) gives (17a) $\emptyset \vdash \mathbb{C} \triangleright E', s$ returns bS . Applying Lemma 17 on (17a) and (15) gives (18a) $\emptyset \vdash \mathbb{C} \llbracket s[N] \rrbracket$. By applying the inductive hypothesis on (18a), (6), we know that (19a) $\emptyset \vdash \mathbb{C}' \llbracket s[N'] \rrbracket$. Applying Lemma 16 to (19a) gives (20a) $\emptyset \vdash \mathbb{C}' \triangleright F$, (21a) $F \vdash s[N']$, and (22a) F sensibly extends \emptyset . Now as $(vs) \in \mathbb{C}$, $(vs) \in \mathbb{C}'$, allowing us to apply Lemma 29 to (20a) giving (23a) $\emptyset \vdash \mathbb{C}' \triangleright F', s$ returns bS . Combining (21a) and (23a), we get (24a) F', s returns $bS \vdash s[N']$. Applying inversion and PROC-THREAD to (24a) gives (25a) F', s returns $bS \ni s$ returns bS and (26a) F', s returns $bS \vdash_s N' : bS$. Applying Lemma 27 to (1), (5), and (7) gives either ((23a) and (28a) F', s returns bS sensibly extends E) or \mathbb{C}' is a shape trap. Now we have already shown that if \mathbb{C}' is a shape trap, then we are done. So let us consider (23a) and (28a) to hold. Applying Lemma 15 and Lemma 29 to (23a), (28a), and (14) give (29a) $\emptyset \vdash \mathbb{C}' \triangleright F', s$ returns aT , and (30a) F', s returns $aT, x : bS \vdash_s M : aT$. Applying STAT-METHOD-CONTEXT to (26a) and (30a) gives (31a) F', s returns $aT \vdash_s \text{val } x =_s \{N'\}M : aT$. Applying PROC-THREAD to F', s returns $aT \ni s$ returns aT and (31a) gives (32a) F', s returns $aT \vdash s[\text{val } x =_s \{N'\}M]$. Applying Lemma 17 to (29a) and (32a) gives (33a) $\emptyset \vdash \mathbb{C}' \llbracket s[\text{val } x =_s \{N'\}M] \rrbracket$. Using the equality of (4) gives $\emptyset \vdash B$, as desired.

Case R-LOCK-CONTEXT: Similar. The only differences are the use of STAT-LOCK-CONTEXT instead of STAT-METHOD-CONTEXT and the handling of the additional assumption from the judgment, namely that E', s returns $aT \vdash p : \square \text{Lock}$. Of course, this can be handled in the same way that (14) E', s returns $aT, x : bS \vdash_s M : aT$ from the previous case is handled.

We now have to prove the proposition is true for the remaining cases. Assume that one of the remaining reduction rules satisfies (2). Let us assume for this case that (3) $A = \mathbb{C} \llbracket A' \rrbracket$ and (4) $B = \mathbb{C}' \llbracket B' \rrbracket$. By inspecting the rule for this case, we know that $\mathbb{C} = \mathbb{C}'$. Thus, we have (5) $B = \mathbb{C} \llbracket B' \rrbracket$. Now there are two cases to consider. Either \mathbb{C} is a shape trap or it isn't. If it is, then by the fact that Lemma 26 holds, $\emptyset \vdash \mathbb{C} \llbracket B' \rrbracket$. Now since (5) $B = \mathbb{C} \llbracket B' \rrbracket$ holds, we have $\emptyset \vdash B$, as desired. Thus, if \mathbb{C} is a shape trap, we are done. Since we arbitrarily chose the reduction rule from the remaining ones, we know that this is the case for all of them. So for each of the remaining cases, we proceed assuming that \mathbb{C} is not a shape trap. We consider each case in turn beginning with the most interesting ones.

Case R-SPECULATION-CLOSE: Assume that R-SPECULATION-CLOSE satisfies (2). Let us assume (3) $A = \mathbb{C} \llbracket \top \Rightarrow A' \rrbracket \llbracket \langle s?p.f=v \rangle \Rightarrow B' \rrbracket$ and (4) $B = \mathbb{C} \llbracket B' \rrbracket$. By the equalities of (3) and (4) combined with (2), we have (5) $\mathbb{C} \llbracket \top \Rightarrow A' \rrbracket \llbracket \langle s?p.f=v \rangle \Rightarrow B' \rrbracket \rightarrow \mathbb{C} \llbracket B' \rrbracket$, and (6) \mathbb{C}, s justifies speculation $p.f = v$. By applying Lemma 16 to (1), we know that (7) $\emptyset \vdash \mathbb{C} \triangleright E$ and (8) $E \vdash \top \Rightarrow A' \rrbracket \llbracket \langle s?p.f=v \rangle \Rightarrow B' \rrbracket$. By inversion and PROC-SPECULATION on (8), we have (9) $E \vdash A'$, (10) $E \vdash \langle s?p.f=v \rangle \triangleright E'$, and (11) $E' \vdash B'$. We note that PROC-SPECULATION-SHAPE-ERROR cannot be applied here since (6) holds. Applying Lemma 20 to (6), (10), and (11) gives (12) $E \vdash B'$. Applying Lemma 17 to (12) and (11) gives (13) $\emptyset \vdash \mathbb{C} \llbracket B' \rrbracket$. By the equality of (4), we know (14) $\emptyset \vdash B$, as desired.

Case R-SPECULATION-OPEN: Assume that R-SPECULATION-OPEN satisfies (2). Let us assume (3) $A = \mathbb{C} \llbracket A' \rrbracket$ and (4) $B = \mathbb{C} \llbracket \top \Rightarrow A' \rrbracket \llbracket \langle s?p.f=v \rangle \Rightarrow A' \rrbracket$. By the equalities of (3) and (4) combined with (2), we have (5) $\mathbb{C} \llbracket A' \rrbracket \rightarrow \mathbb{C} \llbracket \top \Rightarrow A' \rrbracket \llbracket \langle s?p.f=v \rangle \Rightarrow A' \rrbracket$, (6) $s \in \text{thrs}(A')$, (7) $p \in \text{objs}(A')$, and (8) $v \in \mathcal{B}^V \cup \text{objs}(A')$. By applying Lemma 16 to (1), we know that (8) $\emptyset \vdash \mathbb{C} \triangleright E$ and (9) $E \vdash A'$. As (6) and (9) hold, we know (10) $s \in \text{dom}(E)$. As (7) and (9) hold, it is the case that (11) $E \ni p : D$. There are two cases to consider.

Case $E \vdash \langle s?p.f=v \rangle \triangleright E'$: Assume (12a) $E \vdash \langle s?p.f=v \rangle \triangleright E'$ for some environment E' . By applying Corollary 8 to (9) with (12a), we get (13a) $E' \vdash A'$. Applying PROC-SPECULATION to (9), (12a), and (13a), we get (14a) $E \vdash \top \Rightarrow A' \rrbracket \llbracket \langle s?p.f=v \rangle \Rightarrow A' \rrbracket$. Applying Lemma 17 to (8) and (14a) gives (15a) $\emptyset \vdash \mathbb{C} \llbracket \top \Rightarrow A' \rrbracket \llbracket \langle s?p.f=v \rangle \Rightarrow A' \rrbracket$. Due the equivalence of (4), (16a) $\emptyset \vdash B$ holds, as desired.

Case $E \not\vdash \langle s?p.f=v \rangle$: Assume $E \not\vdash \langle s?p.f=v \rangle$. Then it must be the case that (12b) either ($\text{ftype}(D.f) = \dots bt \dots$ and $\neg(v \text{ inhabits } bt)$) or ($\text{ftype}(D.f) = \dots D' \dots$ and $E \ni v : D'$ and $D' \neq D$). Applying PROC-SPECULATION-SHAPE-ERROR to (10), (11), (9), and (12b), we have (13b) $E \vdash \top \Rightarrow A' \rrbracket \llbracket \langle s?p.f=v \rangle \Rightarrow A' \rrbracket$. Applying Lemma 17 to (8) and (13b) gives (14b) $\emptyset \vdash \mathbb{C} \llbracket \top \Rightarrow A' \rrbracket \llbracket \langle s?p.f=v \rangle \Rightarrow A' \rrbracket$. Due the equivalence of (4), (15b) $\emptyset \vdash B$ holds, as desired.

As we showed that each case satisfies the proposition, we know that R-SPECULATION-OPEN satisfies the proposition.

Case R-START: Assume that R-START satisfies (2). Let us assume (3) $A = \mathbb{C} \llbracket A' \rrbracket$, (4) $B = \mathbb{C} \llbracket B' \rrbracket$, where $A' = \text{free } \ell \mid s[\text{val } x = p.\text{start}(); M] \mid \text{runnable } p$ and $B' = \langle s!\ell:0 \rangle \langle s!\ell:1 \rangle (s[M \llbracket \text{unit}/s \rrbracket] \mid \langle p!\ell:2 \rangle p[N \llbracket p/\text{this} \rrbracket])$. By the equalities of (3) and (4) combined with (2), we have (5) $\mathbb{C} \llbracket \text{free } \ell \mid s[\text{val } x = p.\text{start}(); M] \mid \text{runnable } p \rrbracket \rightarrow \mathbb{C} \llbracket \langle s!\ell:0 \rangle \langle s!\ell:1 \rangle (s[M \llbracket \text{unit}/s \rrbracket]) \rrbracket$. Further, by the assumptions of the rule, we have (6) $\mathbb{C} \ni p : D$, (7) $\text{mbody}(D.\text{run}) = \lambda \bar{v}.N$, (8) D not reserved, and (9) $\mathbb{C} \ni \ell : \text{Lock}$. By applying Lemma 16, inversion and PROC-PAR two times, and inversion and PROC-THREAD on (1) and (3), we have (10) $\emptyset \vdash \mathbb{C} \triangleright E$, (11) E is a sensible extension of \emptyset , (12) $E \vdash \text{free } \ell$, (13) $E \vdash \text{runnable } p$, (14) $E \ni s$ returns aT and (15) $E \vdash_s \text{val } x = p.\text{start}(); M : aT$. By (14), we know (16) $s \in \text{dom}(E)$.

We now type the synchronization actions. By applying inversion on PROC-RUNNABLE and PROC-FREE to (13) and (14), we have (17) $E \vdash \diamond$, (18) $p \in \text{dom}(E)$, (19) $E \ni \ell : \text{Lock}$. Applying ACT-ACQUIRE, ACT-RELEASE, and ACT-ACQUIRE again on (14), (18), and (19), we get (20) $E \vdash \langle s!\ell:0 \rangle \triangleright F, s : \text{lock } \ell$, (21) $F, s : \text{lock } \ell \vdash \langle s!\ell:1 \rangle \triangleright F'$, and (22) $F' \vdash \langle p!\ell:2 \rangle \triangleright F''$, where

- $F = E, s:Z, s:effects(\ell, E)$,
- $Z = \{\zeta \mid E \ni \text{onacq } \Phi \zeta \text{ and } E \Vdash_s \Phi\}$,
- $F' = F, \ell:effects(s, F)$,
- $F'' = F', p:Z', p:\text{lock } \ell, p:effects(\ell, F')$,
- $Z' = \{\zeta \mid F' \ni \text{onacq } \Phi \zeta \text{ and } F' \Vdash_p \Phi\}$.

We now type $p[N\{\{p/\text{this}\}\}]$ and $s[M\{\{unit/s\}\}]$. By the definitions of F, F', F'' , (6), and (18), $F'' \vdash p : \boxtimes D$. Noting that the type of $mtype(D.run()) = \boxtimes; \emptyset \rightarrow \square \text{Unit}$, we can apply Lemma 24 on (7) followed by PROC-THREAD to get (23) $F'' \vdash p[N\{\{p/\text{this}\}\}]$. Starting from (15), applying inversion and STAT-METHOD, Lemma 24, and PROC-THREAD gives (24) $E \vdash s[M\{\{unit/s\}\}]$.

We can combine the processes and actions as follows. Starting with (23), we can apply PROC-ACTION on (22), (21), and (20), followed by PROC-PAR on (24). This gives (25)

$E \vdash s[M\{\{unit/s\}\}] \mid \langle s!\ell:0 \rangle \langle s!\ell:1 \rangle \langle p!\ell:2 \rangle p[N\{\{p/\text{this}\}\}]$. Applying Proposition 25 on (25) and the result of applying S-PREFIX-PAR twice on the process in (25) gives (26)

$E \vdash \langle s!\ell:0 \rangle \langle s!\ell:1 \rangle (s[M\{\{unit/s\}\}] \mid \langle p!\ell:2 \rangle p[N\{\{p/\text{this}\}\}])$.

Applying Lemma 17 on (10) and (26) gives (27)

$\emptyset \vdash \mathbb{C}[\langle s!\ell:0 \rangle \langle s!\ell:1 \rangle (s[M\{\{unit/s\}\}] \mid \langle p!\ell:2 \rangle p[N\{\{p/\text{this}\}\}])]$. By applying the equality of (4) to (27), we have (28) $\emptyset \vdash B$, as desired.

Case R-NEW: Assume that R-NEW satisfies (2). Let us assume (3) $A = \mathbb{C}[A']$, (4) $B = \mathbb{C}[B']$, where $A' = \text{free } p \mid s[\text{val } x = \text{new } D(\vec{a})(\vec{v}); M]$ and $B' =$

$\text{runnable } p \mid \langle s!p.\vec{f}=\vec{v} \rangle \langle s!p.\vec{g}=\text{null} \rangle \langle s!p \rangle s[M\{\{p/s\}\}]$. By the equalities of (3) and (4) combined with (2), we have (5) $\mathbb{C}[\text{free } p \mid s[\text{val } x = \text{new } D(\vec{a})(\vec{v}); M]] \rightarrow$

$\mathbb{C}[\text{runnable } p \mid \langle s!p.\vec{f}=\vec{v} \rangle \langle s!p.\vec{g}=\text{null} \rangle \langle s!p \rangle s[M\{\{p/s\}\}]]$. Further, by the assumptions of the rule, we have (6) $\mathbb{C} \ni p:D$, (7) $\text{finals}(D) = \vec{f}$, (8) $\text{nonfinals}(D) = \vec{g}$, and (9) D not reserved. By applying Lemma 16, inversion and PROC-PAR, and inversion and PROC-THREAD on (1) and (3), we have (7) $\emptyset \vdash \mathbb{C} \triangleright E$, (8) E is a sensible extension of \emptyset , (9) $E \vdash \text{free } p$, (10) $E \ni s \text{ returns } aT$ and (11) $E \Vdash_s \text{val } x = \text{new } D(\vec{a})(\vec{v}); M : aT$. Applying STAT-NEW to (11) gives (12) $ftype(D.\vec{f}) = \text{final } \vec{b}\vec{S}$, (13) $E \Vdash_s \vec{v} : \vec{b}\vec{S}$, and (14) $E.x : \boxtimes D \Vdash_s M : aT$. Applying inversion and PROC-FREE on (9) and by (10), we have $E \vdash \infty$, (15) $p \in \text{dom}(E)$ and (16) $s \in \text{dom}(E)$. We can get the write actions on the final fields by using either ACT- \square -RACING-FINAL-WRITE or ACT- \boxtimes -FINAL-WRITE on (16), (15) with (6), (12), and (13), giving (17) $E \vdash \langle s!p.\vec{f}=\vec{v} \rangle \triangleright E$. In a similar way, we can get the non-final write actions by using one of ACT- \square -GUARDED-WRITE or ACT- \boxtimes -GUARDED-WRITE or ACT- \square -RACING-FINAL-WRITE, giving (18) $E \vdash \langle s!p.\vec{g}=\text{null} \rangle \triangleright E$. Applying ACT-BEGIN to (15) and (16) gives (19) $E \vdash \langle s!p \rangle \triangleright E, s : \lambda p \vec{s}$. Given (19), with VAL-OBJECT- \boxtimes , we can show that (20) $E \vdash p : \boxtimes D$, which in turn gives (21) $E, s : \lambda p \vec{s} \vdash p : \boxtimes D$ and (22) $E, s : \lambda p \vec{s} \vdash s[M\{\{p/s\}\}]$. Applying PROC-THREAD gives (23) $E, s : \lambda p \vec{s} \vdash s[M\{\{p/s\}\}]$. Applying PROC-ACTION to (23), (19), (18), and (17) followed by PROC-PAR on the result of PROC-RUNNABLE applied to (15) gives (24)

$E \vdash \text{runnable } p \mid \langle s!p.\vec{f}=\vec{v} \rangle \langle s!p.\vec{g}=\text{null} \rangle \langle s!p \rangle s[M\{\{p/s\}\}]$. Applying Lemma 17 on (7) and (24) gives (25)

$\emptyset \vdash \mathbb{C}[\text{runnable } p \mid \langle s!p.\vec{f}=\vec{v} \rangle \langle s!p.\vec{g}=\text{null} \rangle \langle s!p \rangle s[M\{\{p/s\}\}]]$. By applying the equality of (4) to (25), we have (26) $\emptyset \vdash B$, as desired.

Case R-FIELD-WRITE: Assume that R-FIELD-WRITE satisfies (2). This gives us (3) $A = \mathbb{C}[s[p.f = v; M]]$, (4) $B = \mathbb{C}[\langle s!p.f=v \rangle s[M]]$. By the equalities of (3) and (4) combined with (2), we have (5) $\mathbb{C}[s[p.f = v; M]] \rightarrow \mathbb{C}[\langle s!p.f=v \rangle s[M]]$. By applying Lemma 16, and inversion and PROC-THREAD on (1) and (3), we have (6) $\emptyset \vdash \mathbb{C} \triangleright E$, E is a sensible extension of \emptyset , (7) $E \ni s \text{ returns } aT$ and (8) $E \Vdash_s s[p.f = v; M] : aT$. There are two possibilities of judgments that can type (8).

Case STAT-GUARDED-WRITE: Assume that STAT-GUARDED-WRITE types (8). By inversion and STAT-GUARDED-WRITE, we know (9a) $E \Vdash_s p : \square D$, (10a) $ftype(D.f) = bS \text{ wrguard } \Psi$, (11a) $E \Vdash_s \Psi\{\{p/\text{this}\}\}$, (12a) $E \Vdash_s v : bS$ and (13a) $E \Vdash_s M : aT$. If $b = \square$, use ACT- \square -RACING-GUARDED-WRITE to type (14a) $E \vdash \langle s!p.f=v \rangle \triangleright E$. Otherwise, use ACT- \boxtimes -GUARDED-WRITE to type (14a) $E \vdash \langle s!p.f=v \rangle \triangleright E$. Applying PROC-THREAD to (7) and (13a) followed by PROC-ACTION on (14a) gives (15a) $E \vdash \langle s!p.f=v \rangle s[M]$. By applying Lemma 17 on (6) and (15a) and using the equality of (4), we have (16a) $\emptyset \vdash B$, as desired.

Case STAT-RACING-WRITE: Assume that STAT-RACING-WRITE types (8). By inversion and STAT-RACING-WRITE, we know (9a) $E \Vdash_s p : \square D$, (10a) $ftype(D.f) = S$, (11a) $E \Vdash_s v : \square S$, and (12a) $E \Vdash_s M : aT$. Use ACT- \square -RACING-FINAL-WRITE to type (13a) $E \vdash \langle s!p.f=v \rangle \triangleright E$. Applying PROC-THREAD to (7) and (12a) followed by PROC-ACTION on (13a) gives (14a) $E \vdash \langle s!p.f=v \rangle s[M]$. By applying Lemma 17 on (6) and (15a) and using the equality of (4), we have (16a) $\emptyset \vdash B$, as desired.

As we proved the subcases for both possible judgments to type (8), we have proved the proposition for the case of R-FIELD-WRITE satisfying (2).

Case R-ATOMIC-NEW: This case is similar to both R-NEW and R-START and can be proven similarly.

First, apply Lemma 16, inversion and PROC-PAR twice, inversion PROC-THREAD to (3) $E \vdash s[\text{val } x = \text{new Atomic}(v); M]$. Then apply inversion and STAT-NEW to (4) $E \Vdash_s \text{val } x = \text{new Atomic}(v); M : aT$. Applying inversion on PROC-FREE and PROC-ALLOCATED to (5) $E \vdash \text{free } \ell$ and (6) $E \vdash \ell:\text{Atomic}\{v;2\}$ along with the typing of v from the premises of inversion and STAT-NEW applied to (4) gives us (5) $E \vdash \ell:\text{Atomic}\{v;2\}$. Similar reasoning for how to add the begin p in R-NEW and how to add the locks in and type the thread in R-START will allow us to get (6) $E \vdash \langle s!\ell \rangle \langle s!\ell:1 \rangle s[M\{\{p/s\}\}]$. Combining (5) and (6) with PROC-PAR and then applying Lemma 17 allows us to have the desired result of $\emptyset \vdash \mathbb{C}[\ell:\text{Atomic}\{v;2\} \mid \langle s!\ell \rangle \langle s!\ell:1 \rangle s[M\{\{p/s\}\}]]$.

Case R-ATOMIC-SET: Similar to R-METHOD-CALL and R-ATOMIC-NEW.

Case R-ATOMIC-GET: Similar to R-METHOD-CALL and R-ATOMIC-NEW.

Case R-ATOMIC-GETANDSET: Similar to R-METHOD-CALL and R-ATOMIC-NEW.

Case R-ATOMIC-COMPAREANDSET-FALSE: Similar to R-METHOD-CALL and R-ATOMIC-NEW.

Case R-ATOMIC-COMPAREANDSET-TRUE: Similar to R-METHOD-CALL and R-ATOMIC-NEW.

Case R-LOCK-NEW: Similar to R-ATOMIC-NEW and R-NEW.

Case R-LOCK-ACQUIRE: Similar to R-METHOD-CALL and R-START, using STAT-LOCK and STAT-LOCK-CONTEXT at the appropriate time.

Case R-LOCK-RELEASE: Similar to R-METHOD-RETURN and R-LOCK-ACQUIRE.

Case R-LOCK-ACQUIRE-REENTRANT: Similar to R-LOCK-ACQUIRE.

Case R-LOCK-RELEASE-REENTRANT: Similar to R-METHOD-RETURN and R-LOCK-RELEASE.

Case R-CONDITION-NEW: Similar to R-ATOMIC-NEW and R-NEW.

Case R-CONDITION-RELEASE: Similar to R-START.

Case R-CONDITION-NOTIFY: Similar to R-METHOD-CALL and R-LOCK-RELEASE.

Case R-CONDITION-ACQUIRE: Similar to R-METHOD-CALL and R-LOCK-ACQUIRE.

Case R-END: Assume that R-END satisfies (2). Let us assume (3) $A = \mathbb{C}[A']$, (4) $B = \mathbb{C}[B']$, where $A' = s[\text{end } p; M]$ and $B' = s[M]$. By the equalities of (3) and (4) combined with (2), we have (5) $\mathbb{C}[s[\text{end } p; M]] \rightarrow \mathbb{C}[s[M]]$, and (6) $\langle t!p \rangle <_{\exists hb}^{C_s} s[-]$. By applying Lemma 16 on (1) and (3), we have (7) $\emptyset \vdash \mathbb{C} \triangleright E$, (8) $E \vdash s[\text{end } p; M]$, and (9) E is a sensible extension of \emptyset . By applying inversion and PROC-THREAD on (8), we get (10) $E \ni s \text{ returns } aT$ and (11) $E \Vdash_s \text{end } p; M : aT$. Applying STAT-END to (11) gives (12) $E \Vdash_s V : \boxtimes D$ and (13) $E \Vdash_s M : aT$. Applying PROC-THREAD to (10) and (13) gives (14) $E \vdash s[M]$. Applying Lemma 17 to (7) and (14) gives (15) $\emptyset \vdash \mathbb{C}[s[M]]$. By the equality of (4) with (15), we have (16) $\emptyset \vdash B$, as desired.

Case R-IF-TRUE: Assume that R-IF-TRUE satisfies (2). Let us assume (3) $A = \mathbb{C}[A']$, (4) $B = \mathbb{C}[B']$, where $A' = s[\text{if } (\text{true}) \{M\} \text{ else } \{N\}]$ and $B' = s[M]$. By the equalities of (3) and (4) combined with (2), we have (5) $\mathbb{C}[s[\text{if } (\text{true}) \{M\} \text{ else } \{N\}]] \rightarrow \mathbb{C}[s[M]]$, and (6) $\langle t!p \rangle <_{\exists hb}^{C_s} s[-]$. By applying Lemma 16 on (1) and (3), we have (7) $\emptyset \vdash \mathbb{C} \triangleright E$, (8) $E \vdash s[\text{if } (\text{true}) \{M\} \text{ else } \{N\}]$, and (9) E is a sensible extension of \emptyset . By applying inversion and PROC-THREAD on (8), we get (10) $E \ni s \text{ returns } aT$ and (11) $E \Vdash_s \text{if } (\text{true}) \{M\} \text{ else } \{N\} : aT$. Applying STAT-IF to (11) gives (12) $E \Vdash_s \text{true} : \square \text{boolean}$, (13) $E \Vdash_s M : aT$, and (14) $E \Vdash_s N : aT$. Applying PROC-THREAD to (10) and (13) gives (15) $E \vdash s[M]$. Applying Lemma 17 to (7) and (15) gives

(16) $\emptyset \vdash \mathbb{C} \llbracket s[M] \rrbracket$. By the equality of (4) with (16), we have (17) $\emptyset \vdash B$, as desired.

Case R-IF-FALSE: Similar.

Case R-CAST: Assume that R-CAST satisfies (2). Let us assume (3) $A = \mathbb{C} \llbracket A' \rrbracket$, (4) $B = \mathbb{C} \llbracket B' \rrbracket$, where $A' = s[\text{val } x = (D)p; M]$ and $B' = s[M\{\! \{^p/x\} \! \}]$. By the equalities of (3) and (4) combined with (2), we have (5) $\mathbb{C} \llbracket s[\text{val } x = (D)p; M] \rrbracket \rightarrow \mathbb{C} \llbracket s[M\{\! \{^p/x\} \! \}] \rrbracket$, and (6) $\mathbb{C} \ni p : D$. By applying Lemma 16, and inversion and PROC-THREAD on (1) and (3), we have (7) $\emptyset \vdash \mathbb{C} \triangleright E$, E is a sensible extension of \emptyset , (8) $E \ni s$ returns aT , and (9) $E \vdash_s \text{val } x = (D)p; M : aT$. Applying inversion and STAT-CAST gives (10) $E \vdash_s p : b\text{Object}$, and (11) $E, x : bD \vdash_s M : aT$. By inversion and VAL-SUB-TYPE, (6), and (10) gives (12) $E \vdash_s p : bD$. Applying Lemma 24 on (11) and (12) followed by PROC-THREAD gives (13) $E \vdash_s M\{\! \{^p/x\} \! \} : aT$ and (14) $E \vdash s[M\{\! \{^p/x\} \! \}]$. By applying Lemma 17 on (7) and (14) and using the equality of (4), we have (15) $\emptyset \vdash B$, as desired.

Case R-METHOD-CALL: Assume that R-METHOD-CALL satisfies (2). Let us assume (3) $A = \mathbb{C} \llbracket s[\text{val } x = p.m(\vec{v}); M] \rrbracket$, (4) $B = \mathbb{C} \llbracket s[\text{val } x = {}_s\{N\{\! \{^p/\text{this}\} \! \}\{\! \{^{\vec{v}}/\vec{y}\} \! \}\}M] \rrbracket$. By the equalities of (3) and (4) combined with (2), we have (5) $\mathbb{C} \llbracket s[\text{val } x = p.m(\vec{v}); M] \rrbracket \rightarrow \mathbb{C} \llbracket s[\text{val } x = {}_s\{N\{\! \{^p/\text{this}\} \! \}\{\! \{^{\vec{v}}/\vec{y}\} \! \}\}M] \rrbracket$. By the assumptions for the rule, we have (6) $\mathbb{C} \ni p : D$, (7) $\text{mbody}(D.m) = \lambda \vec{y}. N$, (8) D not reserved, and (9) $m \neq \text{start}$. By applying Lemma 16, and inversion and PROC-THREAD on (1) and (3), we have (10) $\emptyset \vdash \mathbb{C} \triangleright E$, E is a sensible extension of \emptyset , (11) $E \ni s$ returns aT and (12) $E \vdash_s s[\text{val } x = p.m(\vec{v}); M] : aT$. Applying inversion and STAT-METHOD and then Lemma 24 on (12) gives (13) $E, x : bS \vdash_s M : aT$, and (14) $E \vdash_s N\{\! \{^p/\text{this}\} \! \}\{\! \{^{\vec{v}}/\vec{y}\} \! \} : bS$. Reordering the environments in (13) and (14), applying STAT-METHOD-CONTEXT and PROC-THREAD gives (15) $E \vdash s[\text{val } x = {}_s\{N\{\! \{^p/\text{this}\} \! \}\{\! \{^{\vec{v}}/\vec{y}\} \! \}\}M]$. By applying Lemma 17 on (10) and (15) and using the equality of (4), we have (16) $\emptyset \vdash B$, as desired.

Case R-METHOD-RETURN: Assume that R-METHOD-RETURN satisfies (2). Then we have $\mathbb{C} \llbracket s[\text{val } x = {}_s\{\uparrow v\}M] \rrbracket \rightarrow \mathbb{C} \llbracket s[M\{\! \{^v/x\} \! \}] \rrbracket$. This case can be handled similarly to the other cases. Apply Lemma 16, inversion and PROC-THREAD, inversion and STAT-METHOD-CONTEXT, inversion and STAT-RETURN, Lemma 24, PROC-THREAD, and then Lemma 17 to get the desired result.

Case R-OPERATOR: Assume that R-OPERATOR satisfies (2). Then we have $\mathbb{C} \llbracket s[\text{val } x = op(\vec{v}); M] \rrbracket \rightarrow \mathbb{C} \llbracket s[M\{\! \{^w/x\} \! \}] \rrbracket$. This case can be handled similarly to R-METHOD-CALL. Apply Lemma 16, inversion and PROC-THREAD, inversion and STAT-OPERATOR, Lemma 24, PROC-THREAD, and then Lemma 17 to get the desired result.

Case R-FIELD-READ: Assume that R-FIELD-READ satisfies (2). Then we have $\mathbb{C} \llbracket s[\text{val } x = pf; M] \rrbracket \rightarrow \mathbb{C} \llbracket s[M\{\! \{^v/x\} \! \}] \rrbracket$. This case can be handled similarly to the other cases. The only twist is that there are three cases to consider. Applying Lemma 16, inversion and PROC-THREAD gives $E \ni s$ returns aT and (3) $E \vdash_s \text{val } x = pf; M : aT$. There are three typing judgments which could type (3), namely STAT-RACING-READ, STAT-FINAL-READ, and STAT-GUARDED-READ. Each case is handled in the same way. Apply Lemma 19 followed by Lemma 24, PROC-THREAD, and Lemma 17 to complete each case.

Since we showed that all reduction rules satisfy the proposition, we know that subject reduction holds. \square